

2N5737 2N5739

2N5738 2N5740

SDT3825 SDT3805

SDT3826 SDT3806

SDT3827 SDT3807

10 AMP

PNP INDUSTRIAL TRANSISTORS

ABSOLUTE MAXIMUM RATINGS

	<u>2N5737</u>	<u>2N5738</u>	<u>2N5739</u>	<u>2N5740</u>
BV_{CBO}	-60 V	-100 V	-60 V	-100 V
BV_{CEO}	-60 V	-100 V	-60 V	-100 V
BV_{EBO}	-5 V	-5 V	-5 V	-5 V
I_C (Max.)	-10 A	-10 A	-10 A	-10 A
I_B (Max.)	-2 A	-2 A	-2 A	-2 A
P_T (100°C Case)	45 W	45 W	20 W	20 W
Operating Junction Temperature	200°C			
Storage Temperature Range	-65°C to 200°C			

ELECTRICAL CHARACTERISTICS (25°C Ambient)

Static

SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	TYPE
V_{CEO} (sus)	$I_B = 0, I_C = -100$ mA	-60	-	-	Volts	2N5737, 2N5739
	$I_B = 0, I_C = -100$ mA	-100	-	-	Volts	2N5738, 2N5740
I_{CEX}	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V	-	-	-10	μ A	All
	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V, $T_C = 150^\circ$ C	-	-	-500	μ A	All
I_{EBO}	$V_{EB} = -5$ V	-	-	-100	μ A	All
h_{FE}	$I_C = -5$ A, $V_{CE} = -5$ V	20	-	80	-	All
V_{CE} (sat)	$I_C = -5$ A, $I_B = -0.5$ A	-	-	-0.5	Volts	All
V_{BE} (sat)	$I_C = -5$ A, $I_B = -0.5$ A	-	-	-1.2	Volts	All

Dynamic

t_d	(See Figure No. 1)	-	1	-	Nsec	All
t_r	(See Figure No. 1)	-	100	-	Nsec	All
t_s	(See Figure No. 1)	-	600	-	Nsec	All
t_f	(See Figure No. 1)	-	125	-	Nsec	All
h_{fe}	$V_{CE} = -10$ V, $I_C = -1$ A, $f = 1$ MHz	10	-	-	-	All
C_{obo}	$V_{CB} = -10$ V, $I_E = 0$, $f = 1$ MHz	-	-	250	pf	All

ADDITIONAL DEVICES

ABSOLUTE MAXIMUM RATINGS

	<u>SDT3805</u> <u>SDT3825</u>	<u>SDT3806</u> <u>SDT3826</u>	<u>SDT3807</u> <u>SDT3827</u>
BV_{CBO}	-40 V	-80 V	-40 V
BV_{CEO}	-40 V	-80 V	-40 V
BV_{EBO}	-5 V	-80 V	-5 V
I_C (Max.)	-10 A	-10 A	-10 A
I_B (Max.)	-2 A	-2 A	-2 A
P_T (100°C Case) TO-3	45 W	45 W	45 W
P_T (100°C Case) TO-66	20 W	20 W	20 W
Operating Junction Temperature	200°C		
Storage Temperature Range	-65°C to 200°C		

ELECTRICAL CHARACTERISTICS (25°C Ambient)

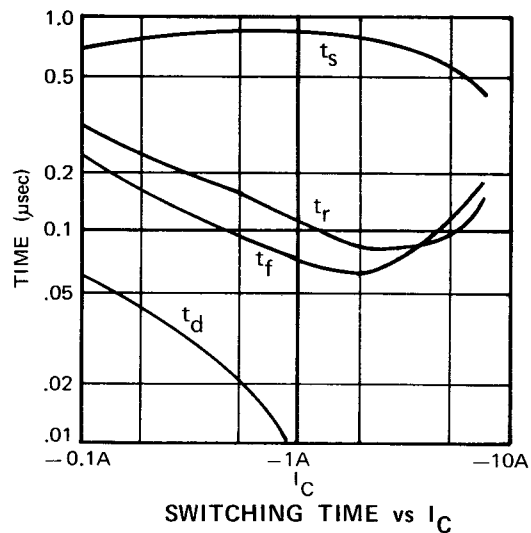
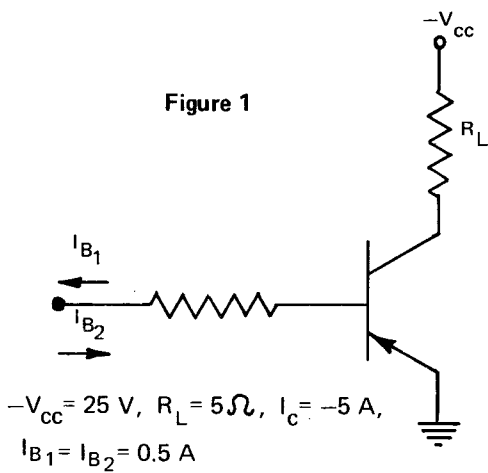
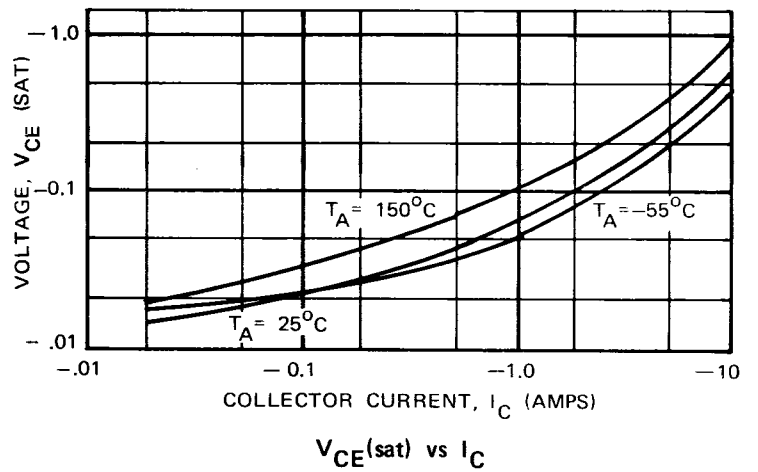
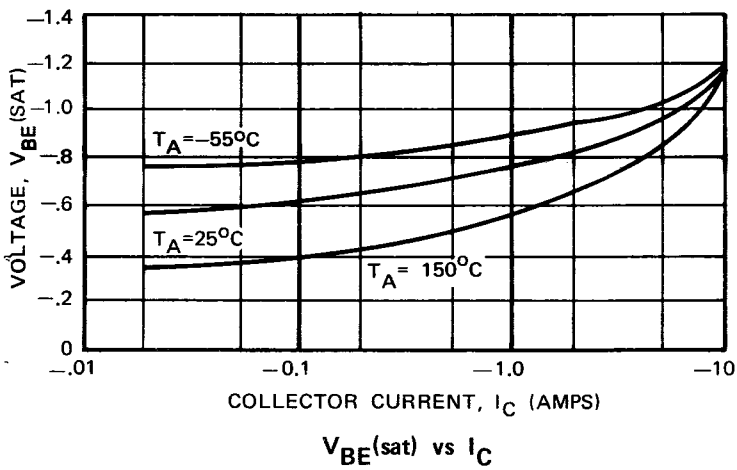
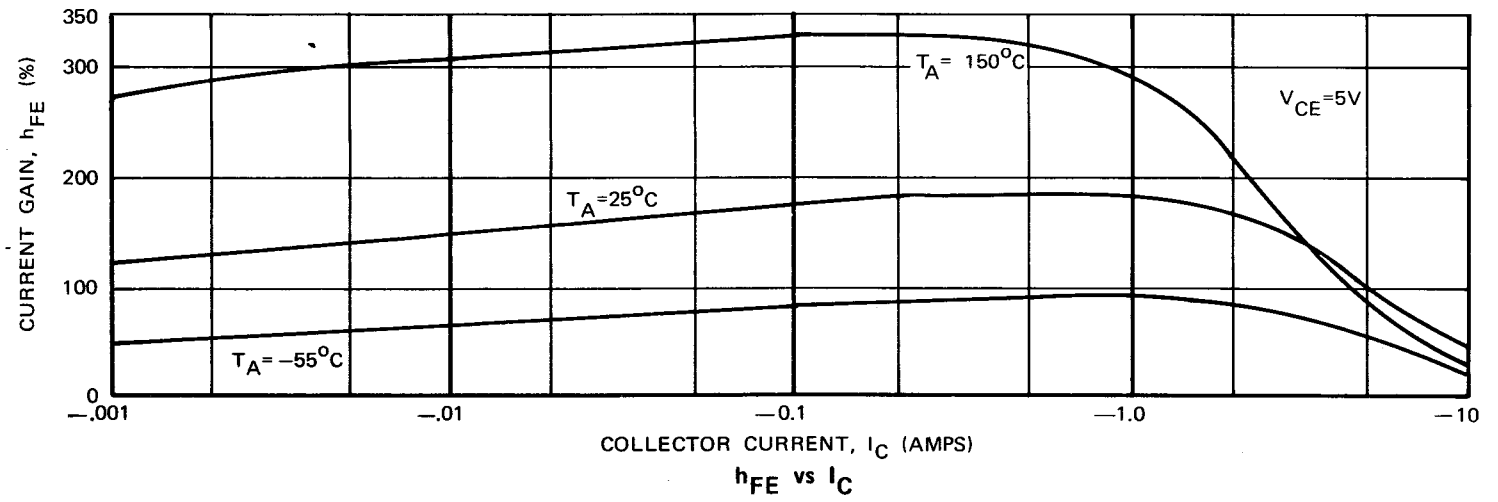
tatic

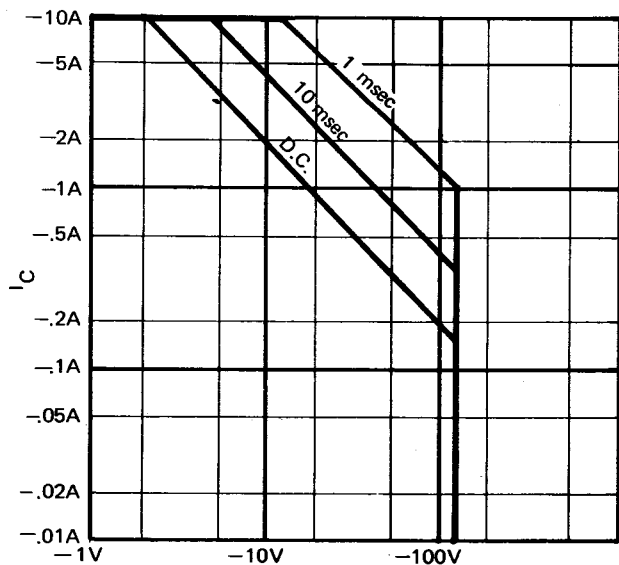
<u>SYMBOL</u>	<u>CONDITIONS</u>	<u>MIN.</u>	<u>TYP.</u>	<u>MAX.</u>	<u>UNITS</u>	<u>TYPE</u>
V_{CEO} (sus)	$I_B = 0, I_C = -100$ mA	-40	-	-	Volts	SDT3805, SDT3825
	$I_B = 0, I_C = -100$ mA	-40	-	-	Volts	SDT3807, SDT3827
	$I_B = 0, I_C = -100$ mA	-80	-	-	Volts	SDT3806, SDT3826
I_{CEX}	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V	-	-	-0.1	mA	All
	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V, $I_C = 150^\circ\text{C}$	-	-	-10	mA	All
I_{EBO}	$V_{EB} = -5$ V	-	-	-1.0	mA	All
h_{FE}	$I_C = -5$ A, $V_{CE} = -5$ V	20	-	80	-	SDT3805, SDT3806 SDT3825, SDT3826
	$I_C = -5$ A, $V_{CE} = -5$ V	40	-	-	-	SDT3807, SDT3827
V_{CE} (sat)	$I_C = -5$ A, $I_B = -0.5$ A	-	-	.75	Volts	All
V_{BE} (sat)	$I_C = -5$ A, $I_B = -0.5$ A	-	-	1.2	Volts	All

ynamic

t_d	(See Figure No. 1)	-	1	-	Nsec	All
t_r	(See Figure No. 1)	-	100	-	Nsec	All
t_s	(See Figure No. 1)	-	600	-	Nsec	All
t_f	(See Figure No. 1)	-	125	-	Nsec	All
h_{fe}	$V_{CE} = -10$ V, $I_C = -1$ A, $f = 1$ MHz	10	-	-	-	All
C_{obo}	$V_{CB} = -10$ V, $I_E = 0$, $f = 1$ MHz	-	-	250	pf	All

CHARACTERISTIC CURVES (ALL TYPES)

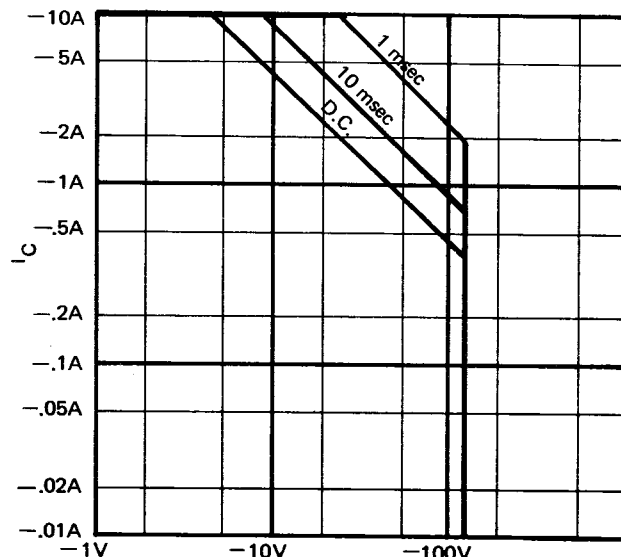




V_{CE}
 $T_C = 100^\circ\text{C}$



TO-66



V_{CE}
 $T_C = 100^\circ\text{C}$



TO-3

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20 AMP

PNP INDUSTRIAL TRANSISTORS

ABSOLUTE MAXIMUM RATINGS

	2N5741	2N5742	2N5743	2N5744
V_{CBO}	-60 V	-100 V	-60 V	-100 V
V_{CEO}	-60 V	-100 V	-60 V	-100 V
V_{EBO}	-5 V	-5 V	-5 V	-5 V
I_C (Max.)	-20 A	-20 A	-20 A	-20 A
I_B (Max.)	-4 A	-4 A	-4 A	-4 A
P_T (100°C Case)	55 W	55 W	24 W	24 W
Operating Junction Temperature	200°C			
Storage Temperature Range	-65°C to 200°C			

ELECTRICAL CHARACTERISTICS (25°C Ambient)

Static

SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	TYPE
V_{CEO} (sus)	$I_B = 0, I_C = -100$ mA	-60	-	-	Volts	2N5741, 2N5743
	$I_B = 0, I_C = -100$ mA	-100	-	-	Volts	2N5742, 2N5744
I_{CEX}	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V	-	-	-10	μ A	All
	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V, $T_C = 150^\circ\text{C}$	-	-	-500	μ A	All
I_{EBO}	$V_{EB} = -5$ V	-	-	-100	μ A	All
h_{FE}	$I_C = -10$ A, $V_{CE} = -5$ V	20	-	80	-	All
V_{CE} (sat)	$I_C = -10$ A, $I_B = -1$ A	-	-	-1.5	Volts	All
V_{BE} (sat)	$I_C = -10$ A, $I_B = -1$ A	-	-	-1.5	Volts	All

Dynamic

t_d	(See Figure No. 1)	-	1	-	Nsec	All
t_r	(See Figure No. 1)	-	200	-	Nsec	All
t_s	(See Figure No. 1)	-	250	-	Nsec	All
t_f	(See Figure No. 1)	-	250	-	Nsec	All
h_{fe}	$V_{CE} = -10$ V, $I_C = -1$ A, $f = 1$ MHz	10	-	-	-	All
C_{obo}	$V_{CB} = -10$ V, $I_E = 0$, $f = 1$ MHz	-	-	500	pf	All

ADDITIONAL DEVICES

ABSOLUTE MAXIMUM RATINGS

	SDT3850 SDT3875	SDT3851 SDT3876	SDT3852 SDT3877
V_{CBO}	-40 V	-80 V	-40 V
V_{CEO}	-40 V	-80 V	-40 V
V_{EBO}	-5 V	-5 V	-5 V
I_C (Max.)	-20 A	-20 A	-20 A
I_B (Max.)	-4 A	-4 A	-4 A
P_T (100°C Case) TO-3	55 W	55 W	55 W
P_T (100°C Case) TO-66	24 W	24 W	24 W
Operating Junction Temperature	200°C		
Storage Temperature Range	-65°C to 200°C		

ELECTRICAL CHARACTERISTICS (25°C Ambient)

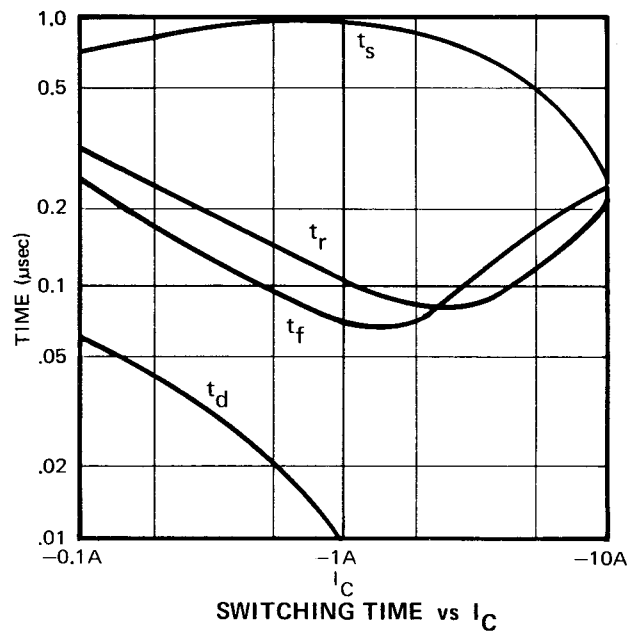
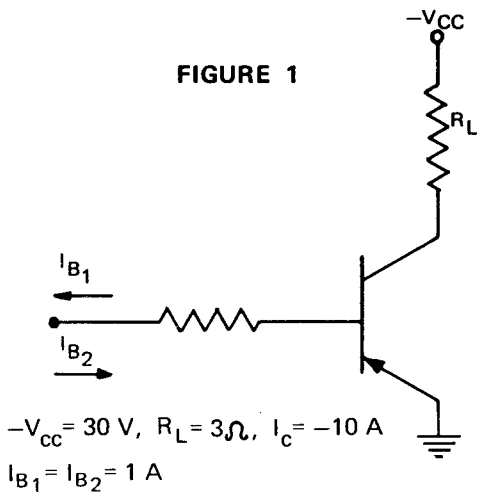
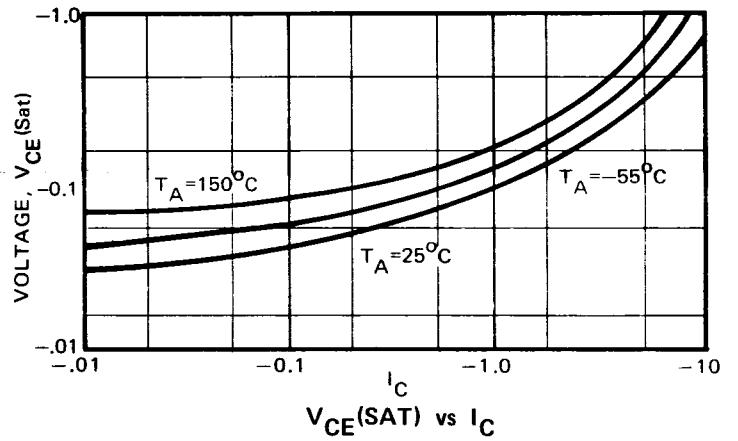
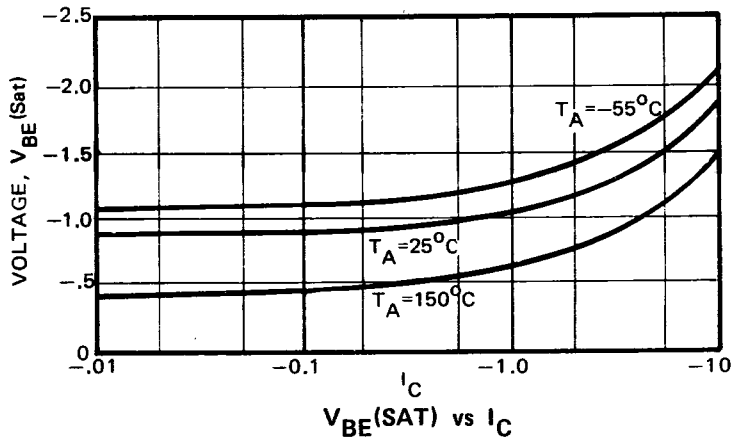
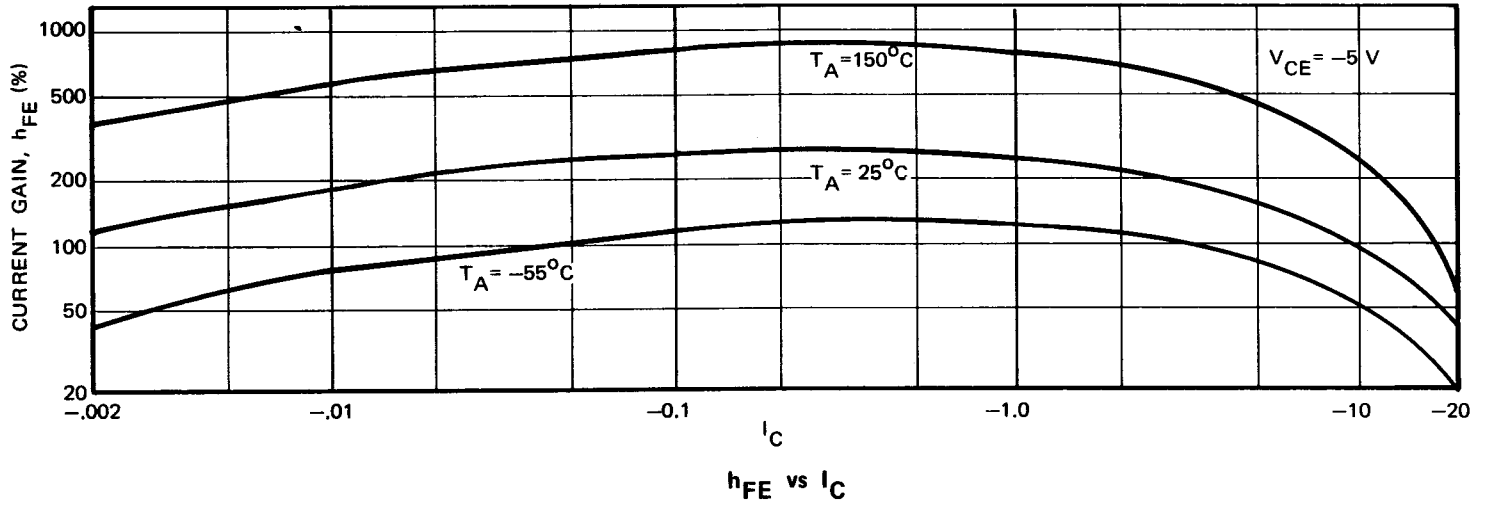
Static

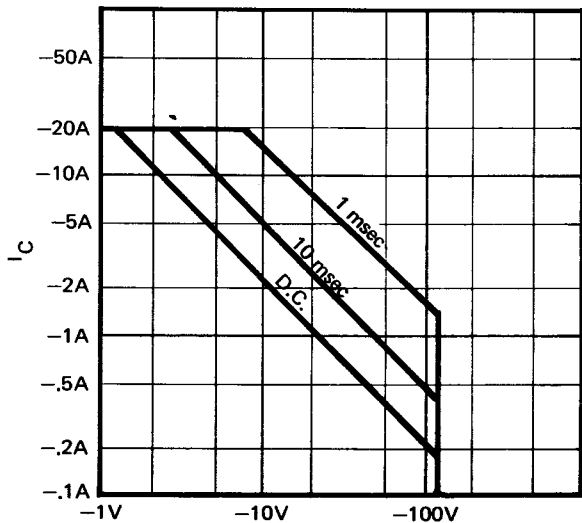
SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	TYPE
$V_{CEO(sus)}$	$I_B = 0, I_C = -100$ mA	-40	-	-	Volts	SDT3850, SDT3875
	$I_B = 0, I_C = -100$ mA	-40	-	-	Volts	SDT3852, SDT3877
	$I_B = 0, I_C = -100$ mA	-80	-	-	Volts	SDT3851, SDT3876
I_{CEX}	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V	-	-	-0.1	mA	All
	$V_{CE} = \text{Rated } V_{CE}, V_{BE} = 1.5$ V, $T_C = 150^\circ\text{C}$	-	-	-10	mA	All
I_{EBO}	$V_{EB} = -5$ V	-	-	-1.0	mA	All
h_{FE}	$I_C = -10$ A, $V_{CE} = -5$ V	20	-	80	-	SDT3850, SDT3851 SDT3875, SDT3876
	$I_C = -10$ A, $V_{CE} = -5$ V	40	-	-	-	SDT3852, SDT3877
$V_{CE(sat)}$	$I_C = -10$ A, $I_B = -1$ A	-	-	-1.8	Volts	All
$V_{BE(sat)}$	$I_C = -10$ A, $I_B = -1$ A	-	-	-2.0	Volts	All

Dynamic

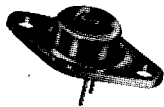
t_d	(See Figure No. 1)	-	1	-	Nsec	All
t_r	(See Figure No. 1)	-	200	-	Nsec	All
t_s	(See Figure No. 1)	-	250	-	Nsec	All
t_f	(see Figure No. 1)	-	250	-	Nsec	All
h_{fe}	$V_{CB} = -10$ V, $I_C = -1$ A, $f = 1$ MHz	10	-	-	-	All
C_{obo}	$V_{CB} = -10$ V, $I_E = 0$, $f = 1$ MHz	-	-	500	pf	All

CHARACTERISTIC CURVES (ALL TYPES)

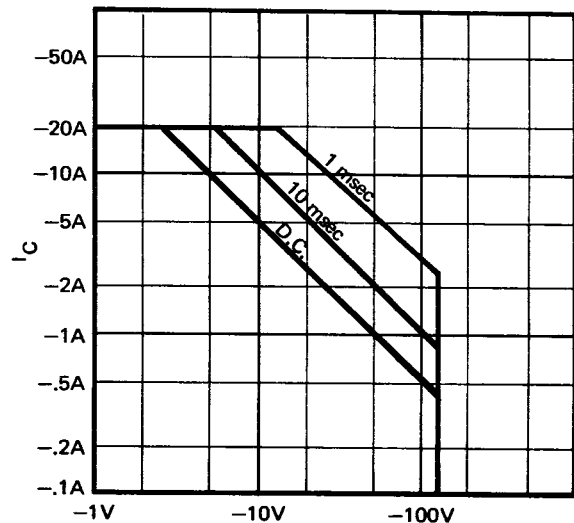




V_{CE}
 $T_c = 100^\circ C$



TO-66



V_{CE}
 $T_c = 100^\circ C$



TO-3

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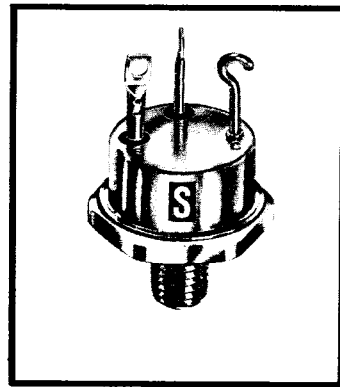
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70 AMP Peak

SILICON NPN POWER TRANSISTORS

FEATURES:

- Multiple diffused planar structure for excellent reliability.
- Excellent high-current gain and switching characteristics, with fast turn-off times.
- Safe Operating Area specified with curves for forward-biased operation and clamped inductive switching.



ABSOLUTE MAXIMUM RATINGS

* Collector-Base Voltage	100 V
* Collector-Emitter Voltage	80 V
* Emitter- Base Voltage	8.0 V
* Peak Collector Current	70 A
* Continuous Collector Current	50 A
* Base Current	10 A
* Storage Temperature	-65°C to +200°C
* Operation Temperature	-65°C to +200°C
* Power Dissipation (100°C Case)	125 W
* Power Dissipation (25°C Case)	219 W
* Lead Temperature 1/16" from case for 10 sec. max. .	250°C
* Thermal Resistance Θ_{j-c}	0.8°C/W
Thermal Time Constant of Chip	35 msec typical

*In accordance with JEDEC registration

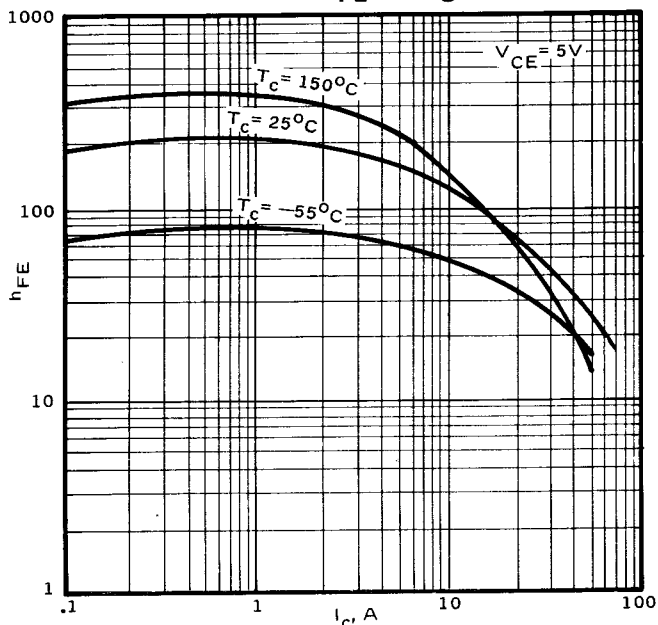
ELECTRICAL CHARACTERISTICS @ 25°C ambient unless otherwise specified

All in accordance with JEDEC registration.

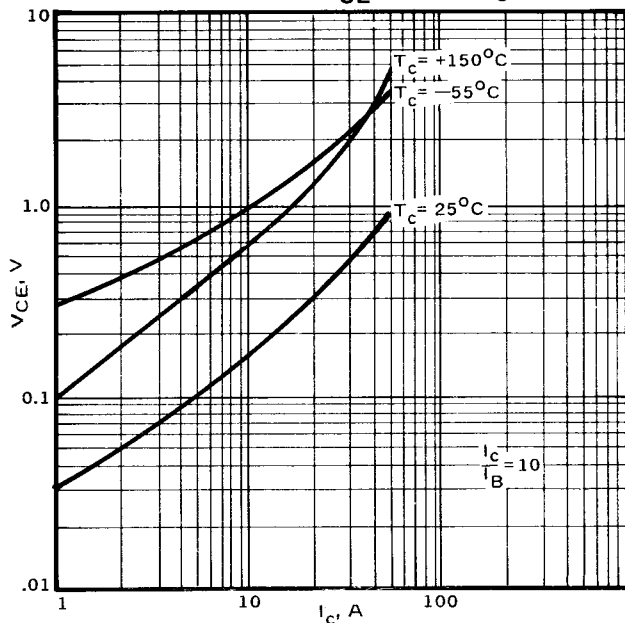
¹Pulsed, $t_p = 300\mu s$, duty cycle=2%

SYMBOL	CONDITIONS	MIN.	MAX.	UNITS
I_{CEV}	$V_{CE} = 80V, V_{EB} = 0.5V, T_A = 150^\circ C$	—	200	μA
I_{CEV}	$V_{CE} = 100V, V_{EB} = 0.5V$	—	200	μA
I_{CBO}	$V_{CB} = 80V$	—	1.0	μA
I_{EBO}	$V_{EB} = 8.0V$	—	200	μA
I_{EBO}	$V_{EB} = 5.0V$	—	1.0	μA
$V_{CEO(sus)}^1$	$I_C = 200mA$	80	—	V
h_{FE}^1	$I_C = 25A, V_{CE} = 2.0V$	25	150	—
h_{FE}^1	$I_C = 50A, V_{CE} = 5.0V$	15	—	—
$V_{CE(sat)}^1$	$I_C = 25A, I_B = 1.25A$	—	0.8	V
$V_{CE(sat)}^1$	$I_C = 50A, I_B = 5.0A$	—	1.5	V
$V_{BE(sat)}^1$	$I_C = 25A, I_B = 1.25A$	—	1.5	V
$V_{BE(sat)}^1$	$I_C = 50A, I_B = 5.0A$	—	2.5	V
t_r	$I_C = 25A, I_{B1} = 1.25A$	—	1.0	μs
t_s	$I_C = 25A, I_{B1} = -I_{B2} = 1.25A$	—	1.0	μs
t_f	$I_C = 25A, I_{B1} = -I_{B2} = 1.25A$	—	0.25	μs
$ h_{fe} $	$I_C = 1.0A, V_{CE} = 10V, f = 10\text{ MHz}$	2.0	—	—

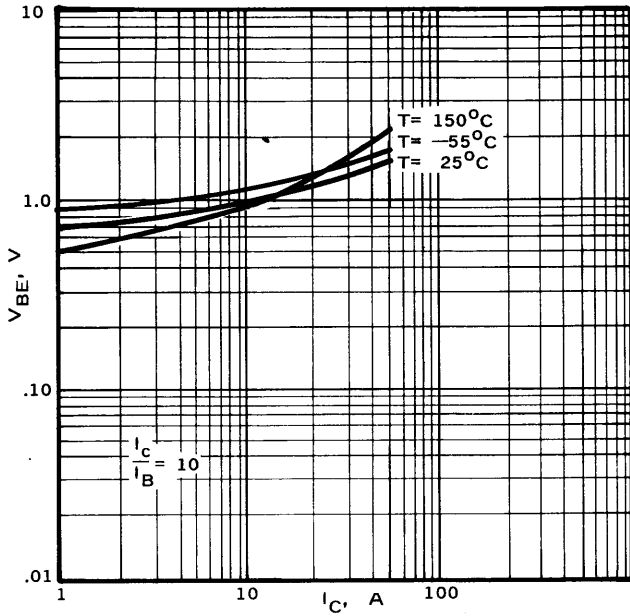
TYPICAL h_{FE} vs I_C



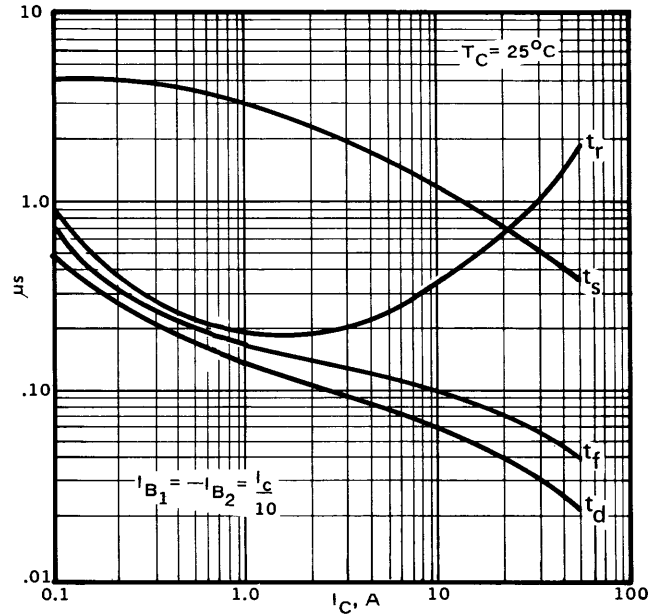
TYPICAL $V_{CE(sat)}$ vs I_C



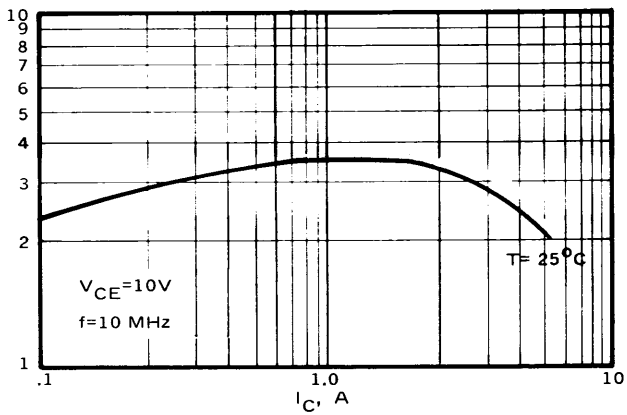
Typical $V_{BE}(sat)$ vs I_C



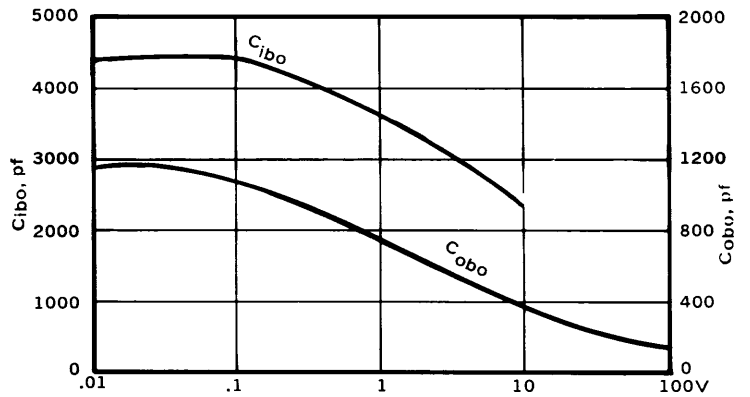
Typical SWITCHING TIME



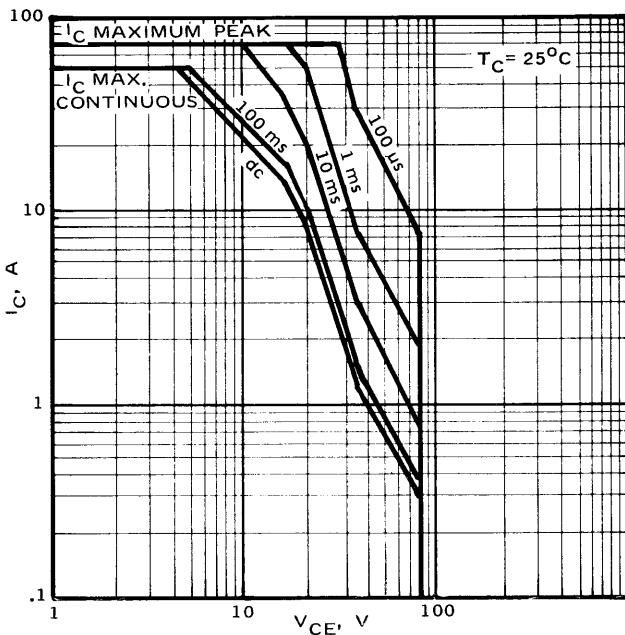
Typical h_{fe} vs I_C



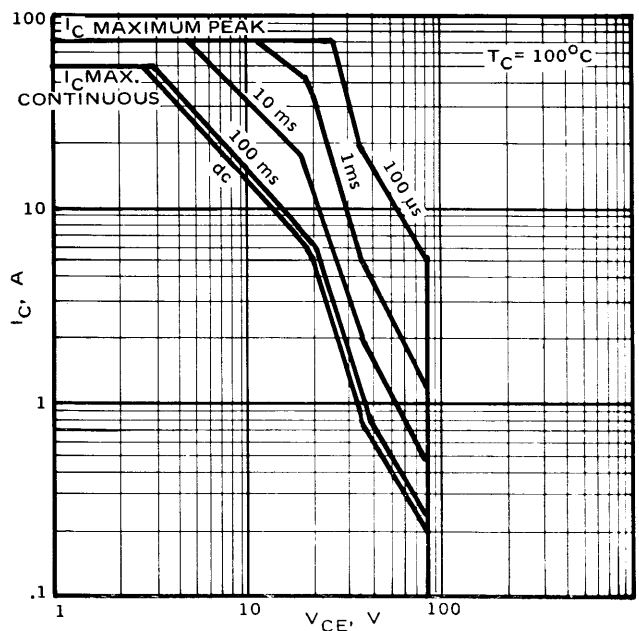
C_{ibo} vs V_{EB} C_{obo} vs V_{CB}



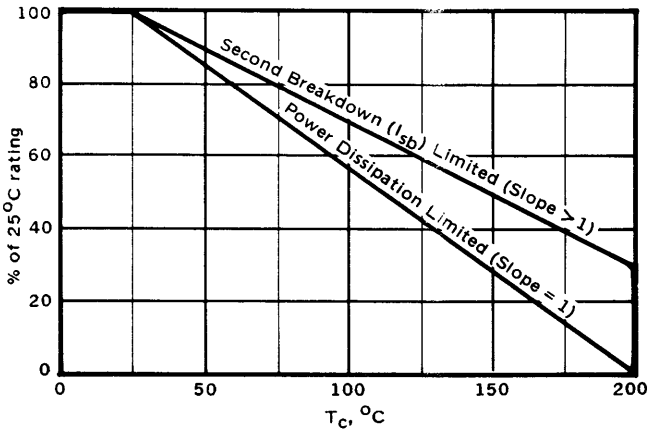
FORWARD-BIASED SAFE AREA



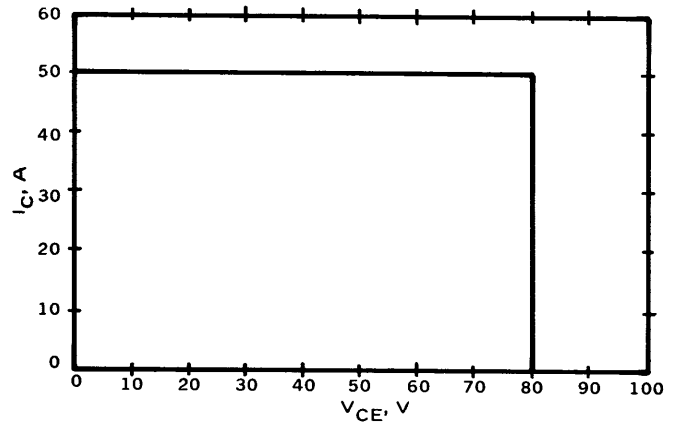
FORWARD-BIASED SAFE AREA



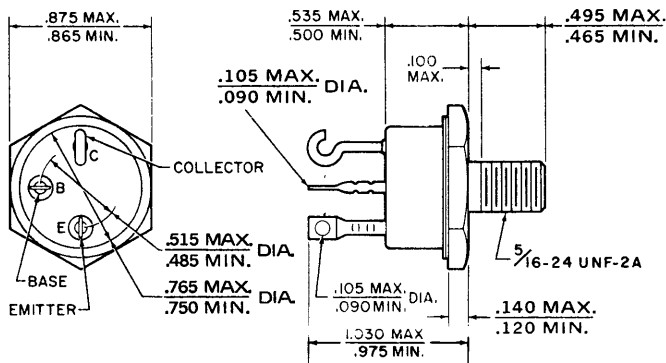
DERATING CURVE
for FORWARD-BIASED SAFE AREA PLOTS



SAFE OPERATING AREA
CLAMPED INDUCTIVE SWITCHING



OUTLINE DIMENSIONS TO-63



NOTES: 1. Collector is electrically common to case.
2. All dimensions in inches.

MOUNTING

It is very important that a power transistor be provided with a good heat dissipating facility. The surface to which the transistor is attached must be flat and free from burrs. The nut must be tightened securely (12 in.-lb. minimum to 20 in.-lb. maximum torque limit when used against a metal chassis or the bushing supplied, provided that all parts are clean and dry).

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Telephone (213) 833-3822, TWX (910) 494-1238

SOUTHWEST
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Dallas, Texas
Telephone (214) 341-1180

MIDWEST
Suite 218, O'Hare Office Center North
2720 Des Plaines Avenue
Des Plaines, Illinois 60018
Telephone (312) 842-8127, TWX (910) 233-2634

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Riviera Beach, Florida 33404
Telephone (305) 848-4311, TWX (510) 952-7610

EUROPE
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Sevenoaks, Kent, England
Telephone (Sevenoaks) 57541/2/3
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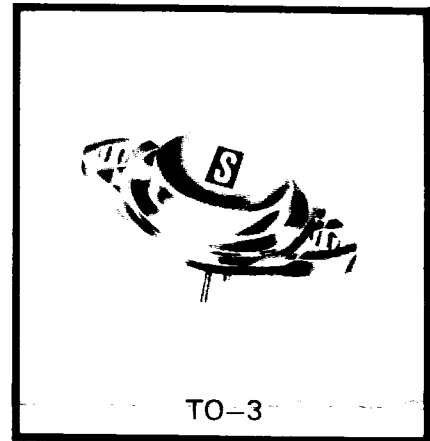
2N6216

2N6217

20 AMP Peak SILICON NPN POWER TRANSISTORS

FEATURES:

- Planar process for excellent reliability
- Low f_T speed to eliminate unwanted oscillations
- Outstanding fast switching characteristics with low storage time.
- Safe Operating Area (SOAR) specified with curves for forward-biased operation and for unclamped inductive switching



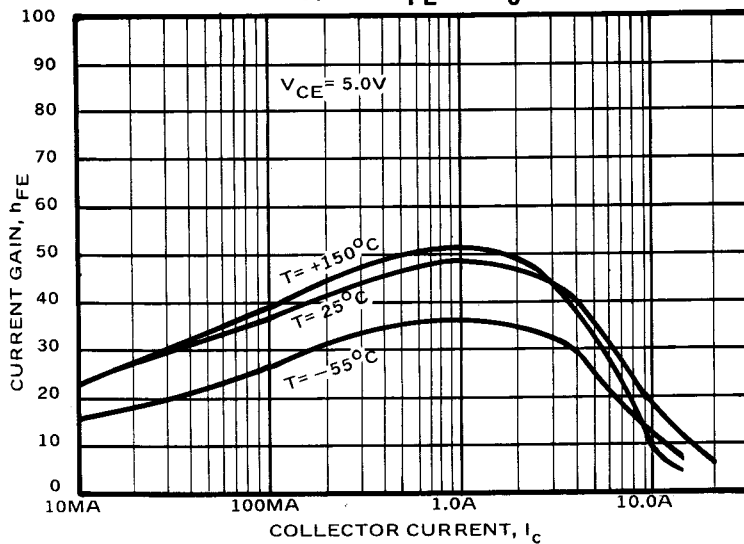
ABSOLUTE MAXIMUM RATINGS

	<u>2N6216</u>	<u>2N6217</u>
Collector-Base Voltage	200 V	140 V
Collector-Emitter Voltage	150 V	80 V
Emitter-Base Voltage	20 V	20 V
Continuous Collector Current	10 A	10 A
Peak Collector Current	20 A	20 A
Continuous Base Current	2 A	2 A
Peak Base Current	4 A	4 A
Storage Temperature	-65°C to +200°C	
Operating Junction Temperature	-65°C to +200°C	
Power Dissipation (100°C Case)	71.4 W	71.4 W
Power Dissipation (25°C Case)	125 W	125 W
Lead Temperature 1/16" from case for 10 seconds max.	250°C	250°C

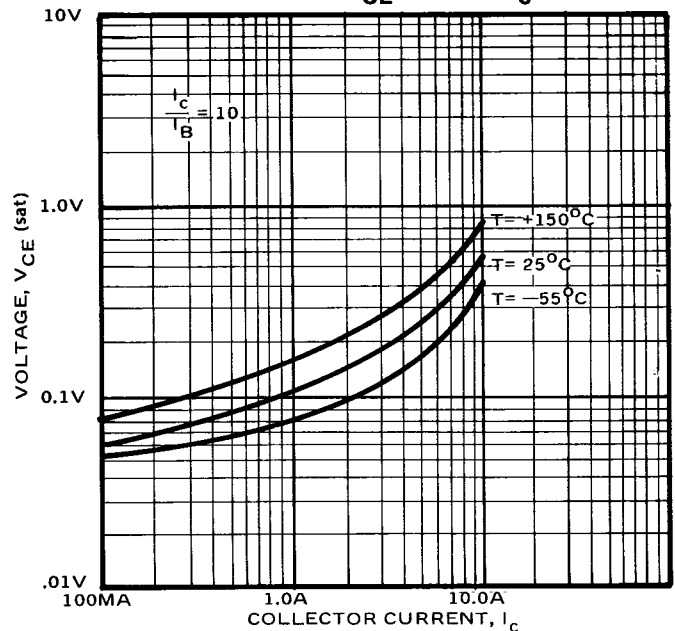
ELECTRICAL CHARACTERISTICS (25°C Ambient unless noted)

SYMBOL	CONDITIONS	MIN	MAX.	UNITS	TYPE
Static					
I_{CEX}	$V_{CE} = 60\text{ V}$, $V_{BE} = -0.5\text{ V}$, $T_C = 150^\circ\text{C}$	—	100	μA	Both
I_{CEX}	$V_{CE} = 200\text{ V}$, $V_{BE} = -0.5\text{ V}$	—	100	μA	2N6216
I_{CEX}	$V_{CE} = 140\text{ V}$, $V_{BE} = -0.5\text{ V}$	—	100	μA	2N6217
I_{CBO}	$V_{CB} = 60\text{ V}$	—	1.0	μA	Both
I_{EBO}	$V_{EB} = 20\text{ V}$	—	100	μA	Both
I_{CEO}	$V_{CE} = 50\text{ V}$	—	10	μA	Both
$V_{CEO}(\text{sus})$	$I_C = 200\text{ mA}$	150	—	Volts	2N6216
$V_{CEO}(\text{sus})$	$I_C = 200\text{ mA}$	80	—	Volts	2N6217
h_{FE}	$I_C = 10\text{ A}$, $V_{CE} = 5.0\text{ V}$	15	—	—	Both
h_{FE}	$I_C = 5.0\text{ A}$, $V_{CE} = 5.0\text{ V}$	20	80	—	Both
$V_{CE}(\text{sat})$	$I_C = 10\text{ A}$, $I_B = 1.0\text{ A}$	—	1.25	Volts	Both
$V_{CE}(\text{sat})$	$I_C = 5.0\text{ A}$, $I_B = 0.5\text{ A}$	—	0.50	Volts	Both
V_{BE}	$I_C = 10\text{ A}$, $V_{CE} = 5.0\text{ V}$	—	1.5	Volts	Both
$V_{BE}(\text{sat})$	$I_C = 5.0\text{ A}$, $I_B = 0.5\text{ A}$	—	1.2	Volts	Both
Dynamic					
C_{ob}	$V_{CB} = 10\text{ V}$	—	250	pf	Both
h_{fe}	$V_{CE} = 5.0\text{ V}$, $I_C = 0.5\text{ A}$, $f = 1\text{ KHz}$	20	—	—	Both
$ h_{fe} $	$V_{CE} = 10\text{ V}$, $I_C = 1\text{ A}$, $f = 1\text{ MHz}$	5.0	—	—	Both
$T_{(ON)}$	$V_{CE} = 20\text{ V}$, $I_C = 5\text{ A}$, $I_B = 0.5\text{ A}$	—	0.8	μsec	Both
$T_{(OFF)}$	$V_{CE} = 20\text{ V}$, $I_C = 5\text{ A}$, $I_B = 0.5\text{ A}$	—	1.0	μsec	Both

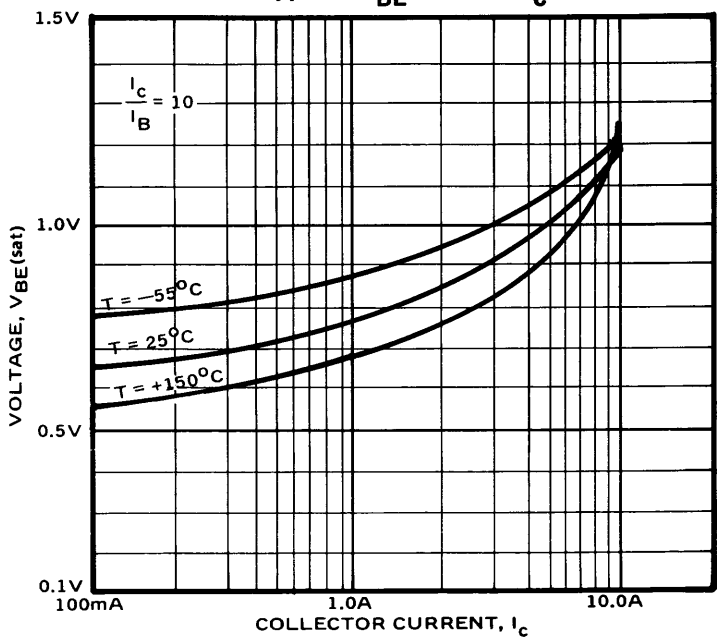
Typical h_{FE} vs I_C



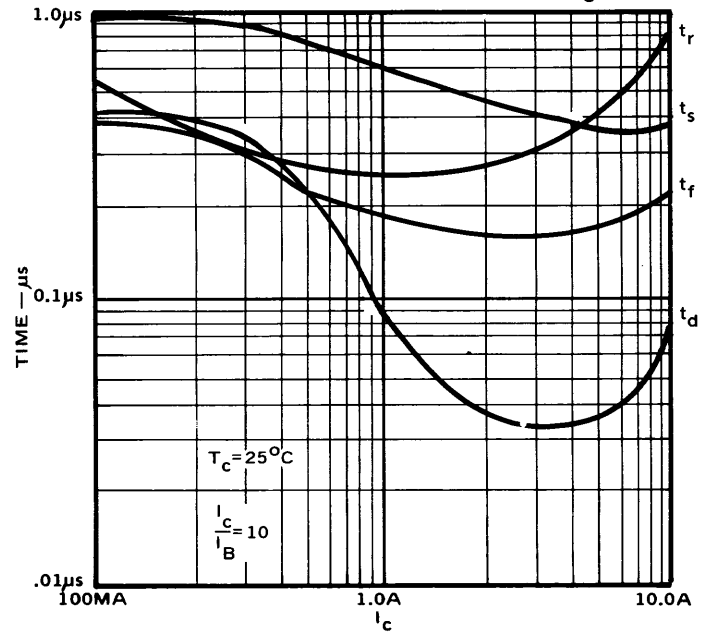
Typical $V_{CE}(\text{sat})$ vs I_C



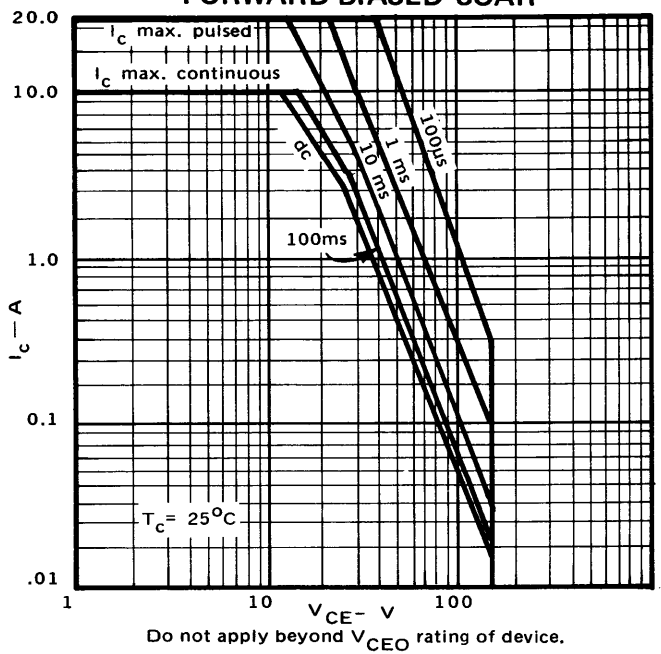
Typical $V_{BE(sat)}$ vs I_C



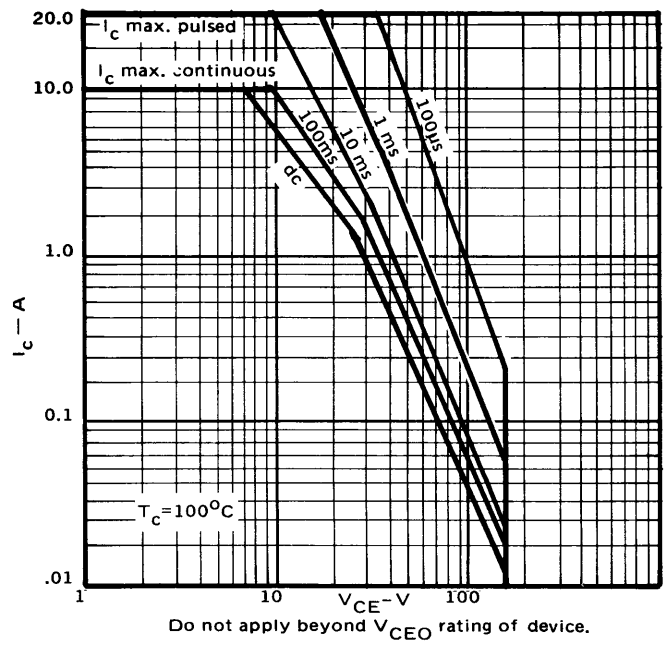
Typical SWITCHING TIME vs I_C



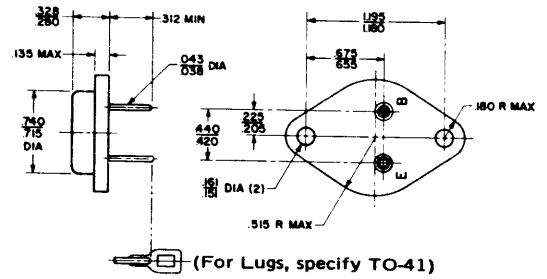
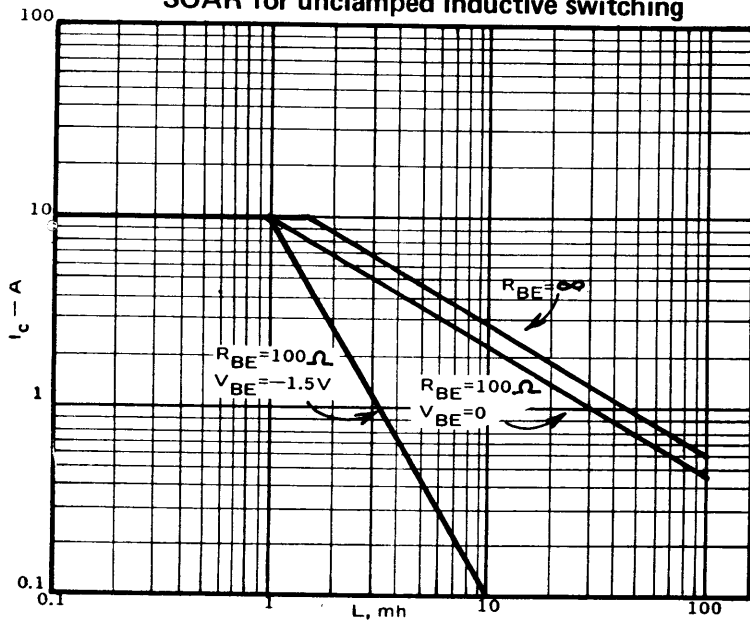
FORWARD-BIASED SOAR



FORWARD-BIASED SOAR



SOAR for unclamped inductive switching



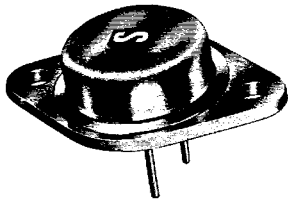
**TO-3
OUTLINE DRAWING**

2N6560

2N6562

FAST SWITCHING

NPN SILICON POWER TRANSISTOR



TO-3

2N6560



TO-61/1

2N6562

ABSOLUTE MAXIMUM RATINGS

@ 25°C (unless otherwise noted)

PARAMETER	2N6560	2N6562	UNITS
V_{CBO}	450	450	V
V_{CEO} (sus)	450	450	V
V_{EBO}	5	5	V
I_C cont.	10	10	A
I_C pk.	15	15	A
I_B	2	2	A
P_T @ 100°C.	125	100	W
T_J	-65°C to +200°C.		
T_S	-65°C to +200°C.		
θ_{J-C}	0.8	1.0	°C/W

*ELECTRICAL CHARACTERISTICS: $T_c = 25^\circ\text{C}$ unless otherwise specified

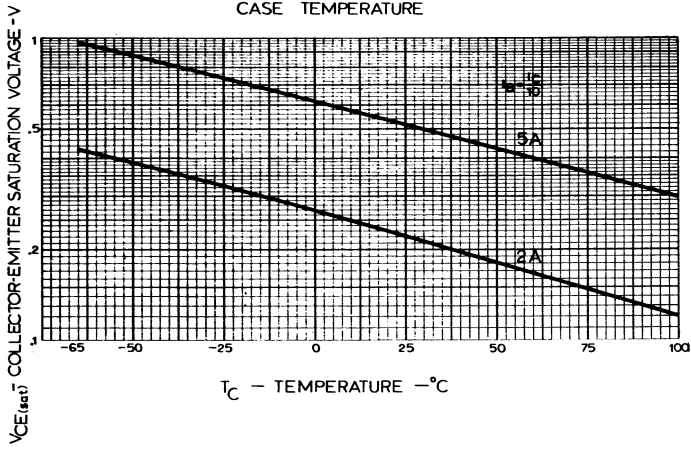
SYMBOL	TEST CONDITIONS							LIMITS				UNITS
	DC COLLECTOR VOLTAGE (V)		DC EMITTER OR BASE VOLTAGE (V)		DC CURRENT (A)			2N6560		2N6562		
	V_{CB}	$-V_{CE}$	V_{EB}	V_{BE}	I_C	I_E	I_B	MIN	MAX	MIN	MAX	
$V_{CEO(sus)}$					0.1			450	—	450	—	V
I_{CEV}		450		-1.5				—	1.0	—	1.0	mA
I_{CEV} $T_c = 100^\circ\text{C}$		335		-1.5				—	1.0	—	1.0	mA
I_{EBO}			5.0					—	1.0	—	1.0	mA
h_{FE}		2.0			5.0			10	40	10	40	
		5.0			15			5	—	5	—	
$V_{CE(sat)}$					5.0		1.0	—	0.75	—	0.75	V
$V_{CE(sat)}$					10		2.0	—	1.0	—	1.0	V
$V_{BE(sat)}$					5.0		1.0	—	1.4	—	1.4	V
f_T $f = 10\text{ MHz}$		10			1.0			10	50	10	50	MHz
C_{obo} $f = 1.0\text{ MHz}$	10							100	450	100	450	pF
t_d		150 ^a			5.0		1.0	—	0.1	—	0.1	μs
t_r		150 ^a			5.0		1.0	—	0.5	—	0.5	μs
t_s		150 ^a			5.0		1.0 1.0 ^b	—	2.5	—	2.5	μs
t_f		150 ^a			5.0		1.0 1.0 ^b	—	0.5	—	0.5	μs
Θ_{J-C}		10			10			—	0.8	—	1.0	$^\circ\text{C/W}$

(a) V_{CC}

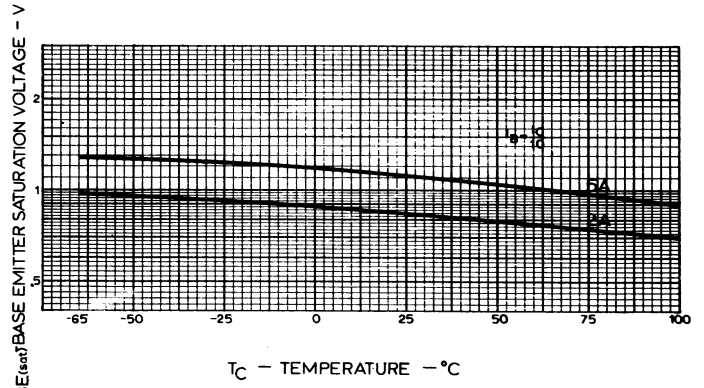
(b) I_{B2}

* IN ACCORDANCE WITH JEDEC REGISTRATION DATA FORMAT ($J_c - 25\text{ RDF} - 1$)

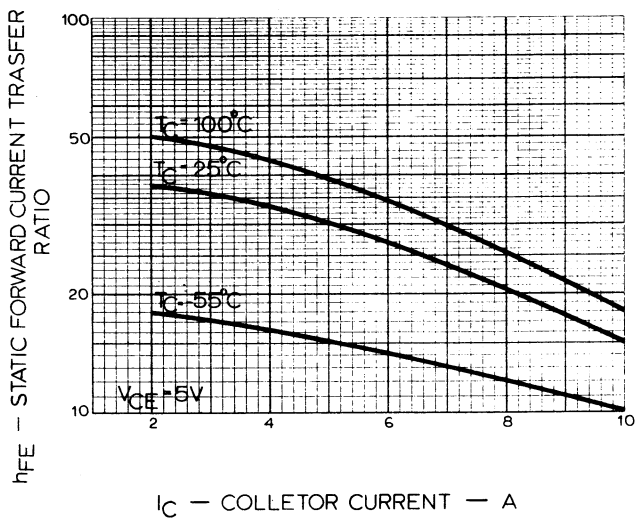
TYPICAL
COLLECTOR-EMITTER SATURATION VOLTAGE
VS
CASE TEMPERATURE



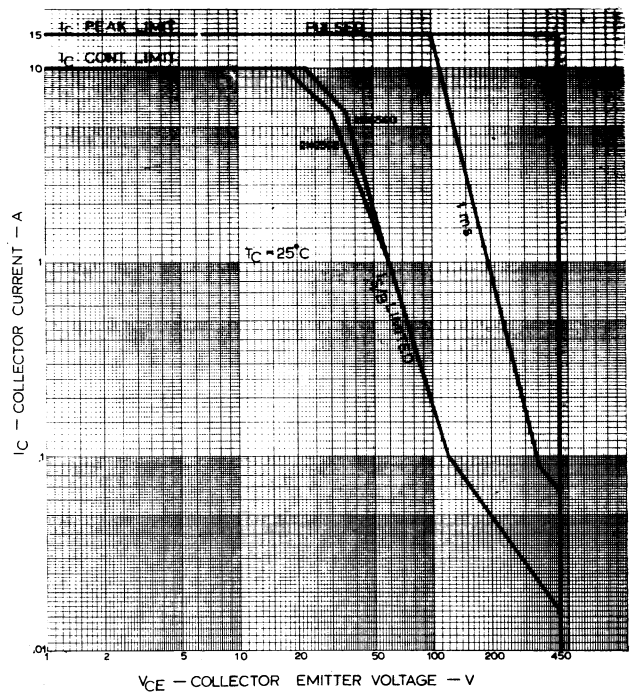
TYPICAL
BASE-EMITTER SATURATION VOLTAGE
VS
CASE TEMPERATURE



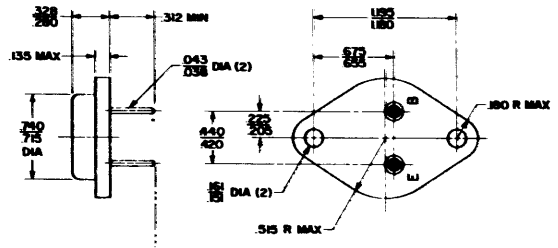
TYPICAL
STATIC FORWARD CURRENT TRANSFER RATIO
VS
COLLECTOR CURRENT



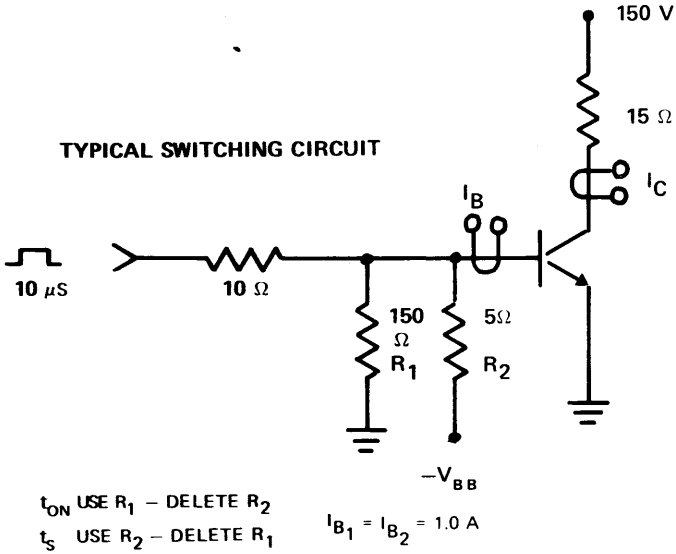
MAXIMUM OPERATING CONDITIONS



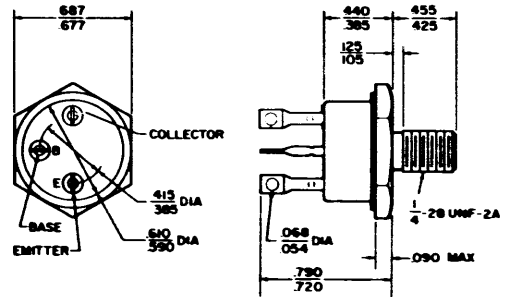
**TO-3
OUTLINE DRAWING**



TYPICAL SWITCHING CIRCUIT



**TO-61
OUTLINE DRAWING**



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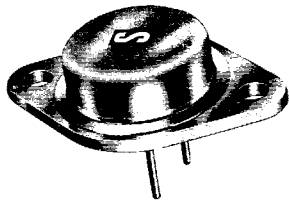
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2N6561

2N6563

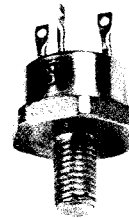
FAST SWITCHING

NPN SILICON POWER TRANSISTOR



TO-3

2N6561



TO-61/I

2N6563

ABSOLUTE MAXIMUM RATINGS

@ 25°C (unless otherwise noted)

PARAMETER	2N6561	2N6563	UNITS
V_{CBO}	300	300	V
V_{CEO} (sus)	300	300	V
V_{EBO}	5	5	V
I_C cont.	10	10	A
I_C pk.	20	20	A
I_B	2	2	A
P_T @ 100 °C.	125	100	W
T_J	-65 °C to +200 °C.		
T_S	-65 °C to +200 °C.		
θ_{J-C}	0.8	1.0	°C/W

*ELECTRICAL CHARACTERISTICS: $T_c = 25^\circ\text{C}$ unless otherwise specified

SYMBOL	TEST CONDITIONS							LIMITS				UNITS
	DC COLLECTOR VOLTAGE (V)		DC EMITTER OR BASE VOLTAGE (V)		DC CURRENT (A)			2N6561		2N6563		
	V_{CB}	V_{CE}	V_{EB}	V_{BE}	I_C	I_E	I_B	MIN	MAX	MIN	MAX	
$V_{CEO(sus)}$					0.1			300	—	300	—	V
I_{CEV}		300		-1.5				—	1.0	—	1.0	mA
I_{CEV} $T_c = 100^\circ\text{C}$		225		-1.5				—	1.0	—	1.0	mA
I_{EBO}			5.0					—	1.0	—	1.0	mA
h_{FE}		2.0			10			10	50	10	50	
		5.0			20			5	—	5	—	
$V_{CE(sat)}$					10		2.0	—	0.75	—	0.75	V
$V_{CE(sat)}$					10		1.0	—	1.0	—	1.0	V
$V_{BE(sat)}$					10		2.0	—	1.8	—	1.8	V
f_T $f = 10\text{ MHz}$		10			1.0			15	60	15	60	MHz
C_{obo} $f = 1.0\text{ MHz}$	10							100	450	100	450	pF
t_d		150 ^a			5.0		1.0	—	0.1	—	0.1	μs
t_r		150 ^a			5.0		1.0	—	0.5	—	0.5	μs
t_s		150 ^a			5.0		1.0 1.0 ^b	—	1.2	—	1.2	μs
t_f		150 ^a			5.0		1.0 1.0 ^b	—	0.5	—	0.5	μs
Θ_{J-C}		10			10			—	0.8	—	1.0	$^\circ\text{C/W}$

(a) V_{CC}

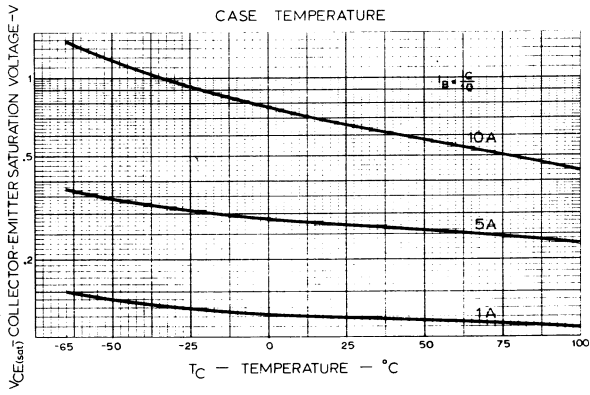
(b) I_{B2}

* IN ACCORDANCE WITH JEDEC REGISTRATION DATA FORMAT ($J_c - 25\text{ RDF} - 1$)

TYPICAL

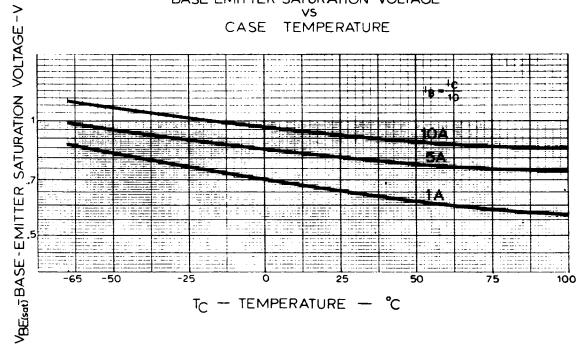
COLLECTOR-EMITTER SATURATION VOLTAGE

VS
CASE TEMPERATURE



TYPICAL

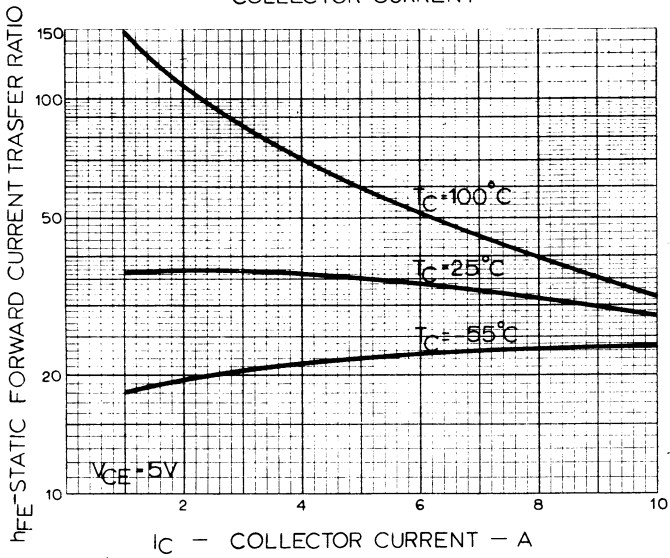
BASE-EMITTER SATURATION VOLTAGE
VS
CASE TEMPERATURE



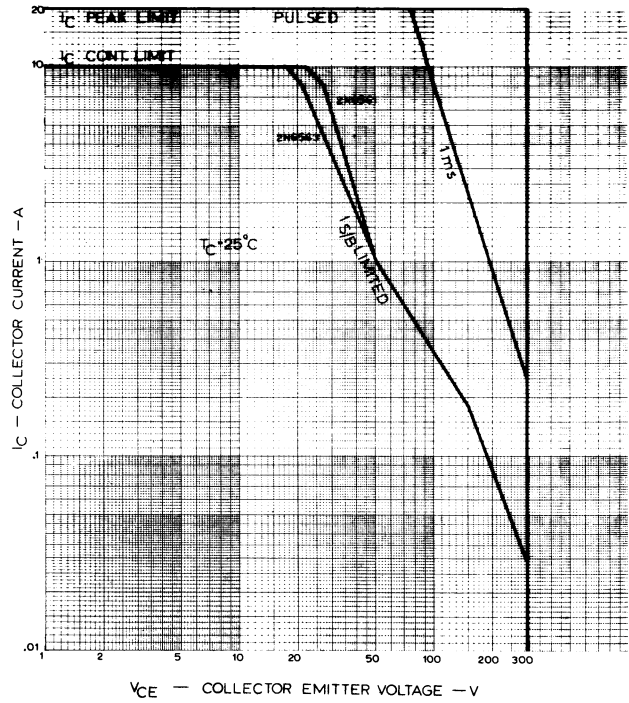
TYPICAL

STATIC FORWARD CURRENT TRANSFER RATIO

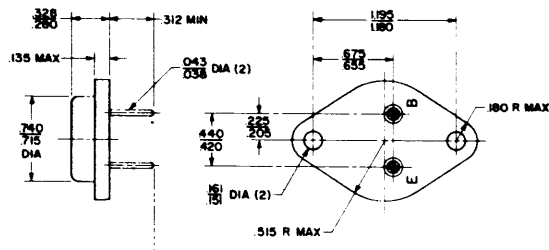
VS
COLLECTOR CURRENT



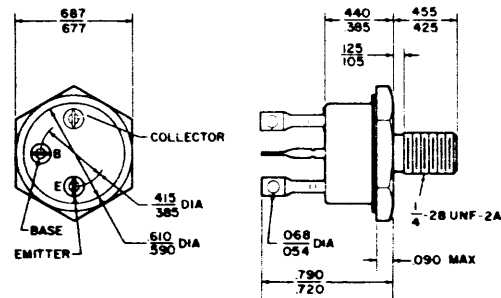
MAXIMUM OPERATING CONDITIONS



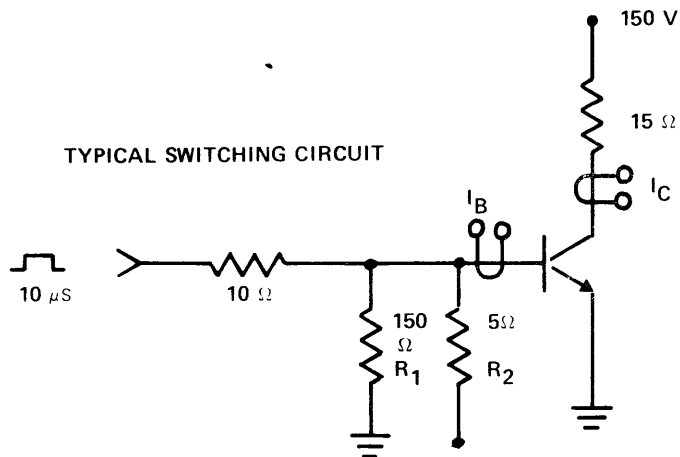
TO-3 OUTLINE DRAWING



TO-61 OUTLINE DRAWING



TYPICAL SWITCHING CIRCUIT



t_{ON} USE R_1 - DELETE R_2
 t_S USE R_2 - DELETE R_1

$$I_{B1} = I_{B2} = 1.0 \text{ A}$$

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