

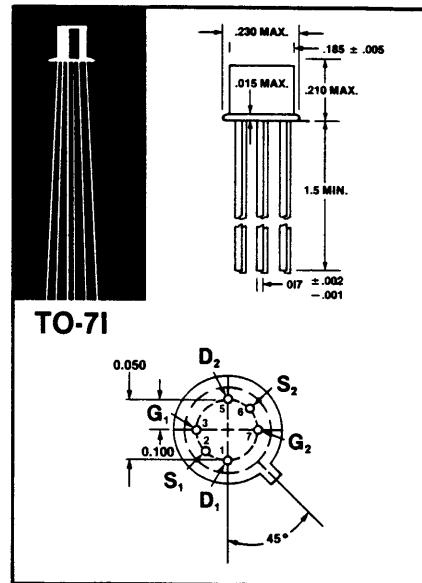
**ULTRA LOW NOISE
DUAL MATCHED
N-CHANNEL FIELD EFFECT TRANSISTOR**

CD860

GEOMETRY 424

**HIGH PERFORMANCE DIFFERENTIAL
AMPLIFIERS**

- $1.4 \text{ nV/Hz}^{1/2}$ en @ 1 kHz
- Min. Operating Gm $25,000 \mu\text{mho}$
- Matched VPO and Gm



The CD860 is a high GM/ID low noise junction F.E.T. for low level amplifier use. The min. GM of 25,000 assures a voltage gain of 25 min. with a 1K drain load. As a source follower it has a typical output impedance of 25 ohms. The 10mA operating point is easily held due to its low pinchoff voltage and is very close to its zero T.C. point for temperature stable operation.

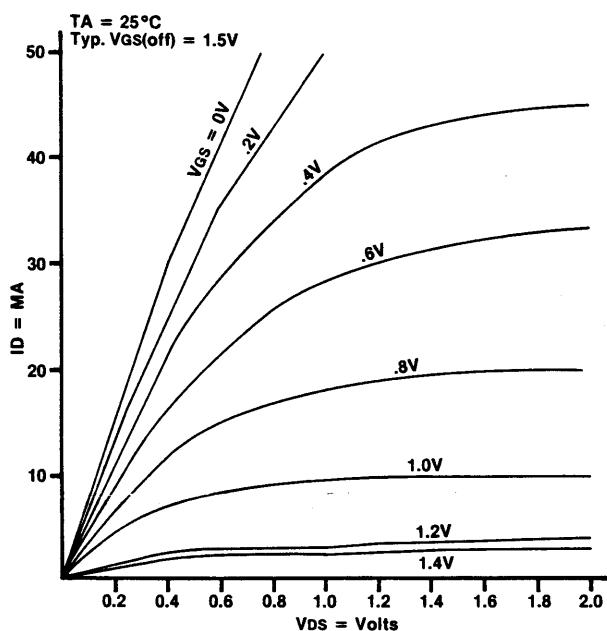
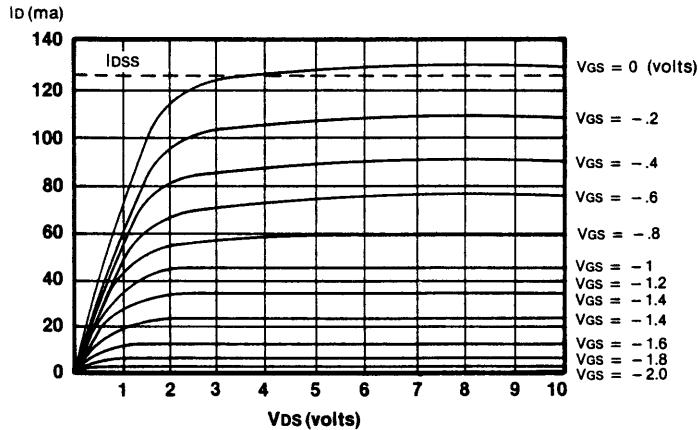
ELECTRICAL DATA ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL		UNITS
Drain to Source Voltage	BV _{DSD}	20	Volts
Drain to Gate Voltage	BV _{DG0}	20	Volts
Gate to Source Voltage	BV _{GSD}	-20	Volts
D.C. Forward Gate Current	I _{GF}	50	mA
Junction Temp. (Operating & Storage)	T _J	-65°C to +200°C	
Power Dissipation (Free Air)	P _D	400 mW	
Lead Temp. (@ 1/16" ± 1/32" from case)	T _L	240° for 10 sec.	
Derating Factor (Free Air)	D _F	2.3 mW/C°	

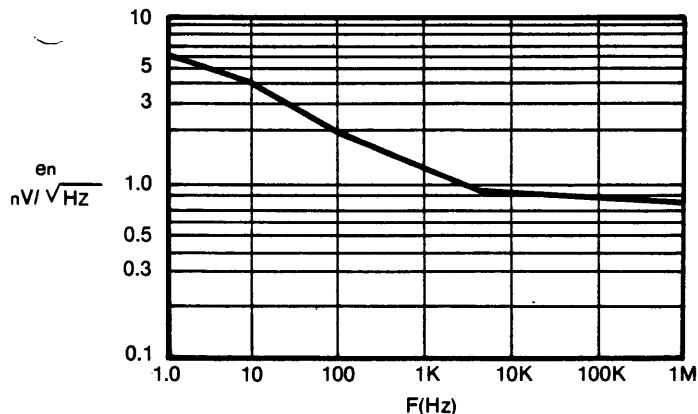
ELECTRICAL CHARACTERISTICS: $T_A = 25^\circ\text{C}$ (UNLESS OTHERWISE STATED)

PARAMETER	SYMBOL	CONDITION	Min.	Typ.	Max.	UNITS
Gate Leakage Current	I _{GSS}	$V_{GS} = -10\text{V}, V_{DS} = 0$		0.1	3.0	nA
Reverse Leakage Current	I _{GSS}	$V_{GS} = -10\text{V}, V_{DS} = 0, T_A = 85^\circ\text{C}$		5	100	nA
Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 10\text{V}, V_{GS} = 0$	10	100		mA
Pinch-Off Voltage	V _{P0}	$V_{DS} = 10\text{V}, I_D = 0.1\text{mA}$	0.3	1.5	3.0	Volts
Transconductance	g _m	$V_{DS} = 10\text{V}, I_D = 10\text{mA}, f = 1\text{kHz}$	25	40		mmho
Input Capacitance	C _{iss}	$V_{DS} = 10\text{V}, I_D = 10\text{mA}, f = 140\text{kHz}$	30	35		pfd
Reverse Xfer Cap	C _{rss}	$V_{DS} = 10\text{V}, f = 140\text{kHz}$	17	20		pfd
Gate to Drain Capacitance	C _{GD}	$V_{GD} = -10\text{V}, f = 140\text{kHz}$	20			pfd
Output Admittance	Y _{os}	$V_{DS} = 10\text{V}, I_D = 10\text{mA}$	50	100		μmho
Input Noise Voltage	e _n	$V_{DS} = 5\text{V}, I_D = 10\text{mA}, f = 1\text{kHz}$	1.4	2.0		$\text{nV/Hz}^{1/2}$
Input Noise Voltage	e _n	$V_{DS} = 5\text{V}, I_D = 10\text{mA}, f = 10\text{Hz}$	6.0	10		$\text{nV/Hz}^{1/2}$
Input Noise Voltage	e _n TOTAL	$V_{DS} = 5\text{V}, I_D = 10\text{mA}, f = 10\text{Hz to } 20\text{kHz}$	0.4	0.6		μVrms
Equivalent Open Ckt.						
Input Noise Current	i _n	$R_{source} < 100\text{K}\Omega, f = 1\text{kHz}$.01		$\text{pA/Hz}^{1/2}$
VPO Match	V _{P0} , V _{P02}	$V_{DS} = 10\text{V}, I_D = 0.1\text{mA}$			25	mV
Gm Match	G _{m1} , G _{m2}	$V_{DS} = 10\text{V}, I_D = 10\text{mA}, f = 1\text{kHz}$			5	%

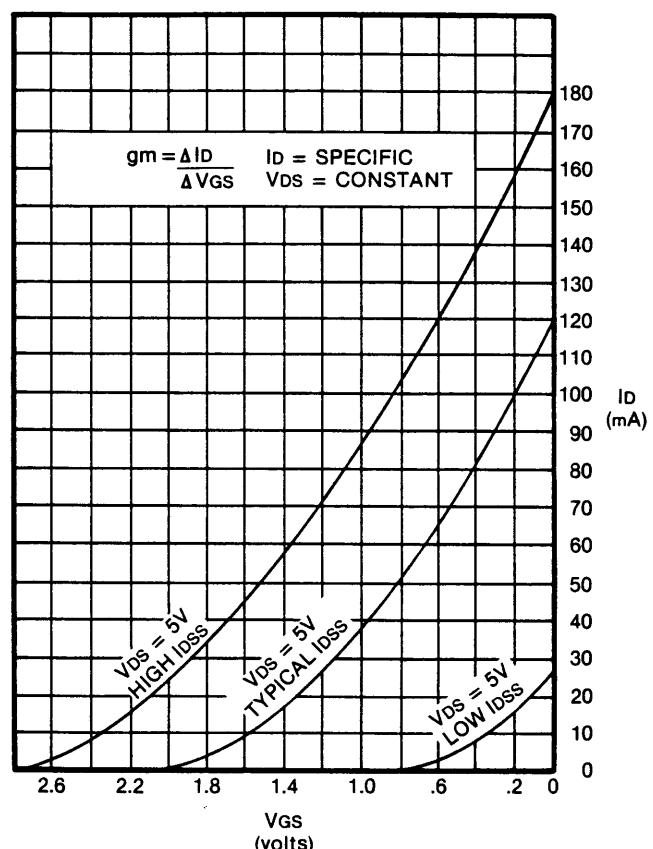
TYPICAL CHARACTERISTIC CURVES



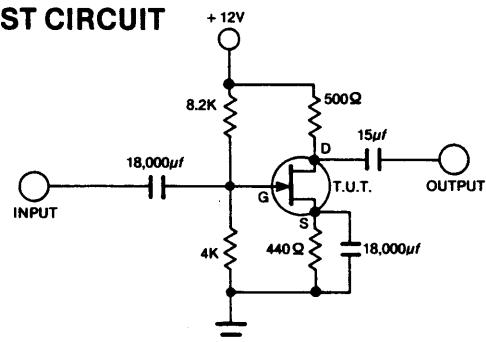
**TYPICAL SHORT CIRCUIT
INPUT NOISE VS. FREQUENCY**



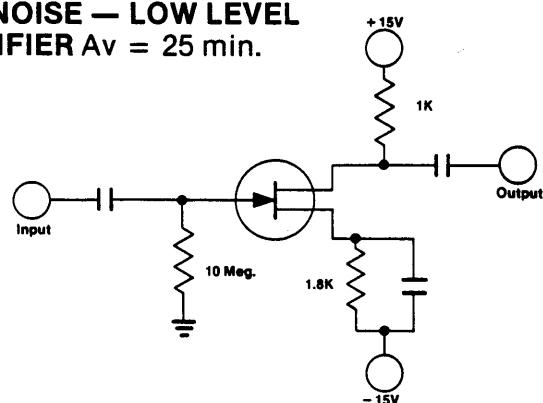
TRANSCONDUCTANCE CHARACTERISTICS



NOISE TEST CIRCUIT



**LOW NOISE — LOW LEVEL
AMPLIFIER $A_v = 25$ min.**



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