



# MP5911 • 5912 • 5912C

## WIDEBAND HIGH GAIN MONOLITHIC DUAL SILICON NITROX® FIELD EFFECT TRANSISTORS

HIGH TRANSCONDUCTANCE  
THROUGH 100MHz .....  $g_{fs} = 4000\mu\text{mho}$   
 LOW INPUT CAPACITANCE  
 MATCHING CHARACTERISTICS  
 ARE SPECIFIED

ABSOLUTE MAXIMUM RATINGS (Note 4)  
 @ 25°C (unless otherwise noted)

**Maximum Temperatures**

Storage Temperature	-65° to	+150°C
Operating Junction Temperature		+150°C
Lead Temperature (Soldering, 10 second time limit)		+200°C

**Maximum Power Dissipation**

Device Dissipation @ Free Air-Total	500mW
-------------------------------------	-------

**Maximum Voltage and Current for Each Transistor**

-V <sub>GSS</sub>	Gate to Drain or Source Voltage	35V
-V <sub>DSS</sub>	Drain to Source Voltage	30V
-I <sub>G(f)</sub>	Forward Current	50mA

**ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)**

SYMBOL	CHARACTERISTIC	MP5911		MP5912		MP5912C		UNIT	CONDITIONS
		Min	Max	Min	Max	Min	Max		
$ I_{G1}-I_{G2} $	Differential Gate Current		20		20		20	nA	V <sub>DG</sub> = 10 V, I <sub>D</sub> = 5 mA T <sub>A</sub> = 125°C
$\frac{I_{DSS1}}{I_{DSS2}}$	Saturation Drain Current Ratio (Notes 1 and 2)	0.95	1	0.95	1	0.95	1	-	V <sub>DG</sub> = 10 V, V <sub>GS</sub> = 0
$ V_{GS1}-V_{GS2} $	Differential Gate-Source Voltage		10		15		40	mV	V <sub>DG</sub> = 10 V, I <sub>D</sub> = 5 mA T <sub>A</sub> = 25°C
$\frac{\Delta V_{GS1}-V_{GS2} }{\Delta T}$	Gate-Source Voltage Differential Drift (note 3)		20		40		40	$\mu\text{V}/^\circ\text{C}$	T <sub>A</sub> = 25°C V <sub>DG</sub> = 10 V T <sub>B</sub> = 125°C I <sub>D</sub> = 5 mA T <sub>A</sub> = -55°C T <sub>B</sub> = 25°C
$\frac{g_{fs1}}{g_{fs2}}$	Transconductance Ratio (Note 2)	0.95	1	0.95	1	0.95	1	-	f = 1 kHz

Notes and Additional Electrical Characteristics on next page.

# MP5911 • 5912 • 5912C

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

SYMBOL	CHARACTERISTICS	MIN.	MAX.	UNITS	CONDITIONS	
$I_{GSS}$	Gate Reverse Current		-50	pA	$V_{GS} = -15\text{ V}, V_{DS} = 0$	$T_A = 125^\circ\text{C}$
			-200	nA		
$BV_{GSS}$	Gate-Source Breakdown Voltage	-25		V	$I_G = -1\ \mu\text{A}, V_{DS} = 0$	
$V_{GS(off)}$	Gate-Source Cutoff Voltage	-1	-5		$V_{DS} = 10\text{ V}, I_D = 1\text{ nA}$	
$V_{GS}$	Gate-Source Voltage	-0.3	-4			
$I_G$	Gate Operating Current		-50	pA	$V_{DG} = 10\text{ V}, I_D = 5\text{ mA}$	$T_A = 125^\circ\text{C}$
			-50	nA		
$I_{DSS}$	Saturation Drain Current (Note 1)	7	40	mA	$V_{DS} = 10\text{ V}, V_{GS} = 0\text{ V}$	
$g_{fs}$	Common-Source Forward Transconductance	4000	10,000	$\mu\text{mho}$	$V_{DG} = 10\text{ V}, I_D = 5\text{ mA}$	f = 1 kHz
$g_{fs}$	Common-Source Forward Transconductance	4000	10,000			f = 100MHz
$g_{os}$	Common-Source Output Conductance		100			f = 1 kHz
$g_{os}$	Common-Source Output Conductance		150			f = 100MHz
$C_{iss}$	Common-Source Input Capacitance		5	pF	$V_{DG} = 10\text{ V}, I_D = 5\text{ mA}$	f = 1 MHz
$C_{rss}$	Common-Source Reverse Transfer Capacitance		1.2			
$\bar{e}_n$	Equivalent Short Circuit Input Noise Voltage		20	$\frac{\text{nV}}{\sqrt{\text{Hz}}}$	$V_{DG} = 10\text{ V}, I_D = 5\text{ mA}$	f = 10 kHz
NF	Spot Noise Figure		1	dB		f = 10 kHz

### Notes:

1. Pulswidth < 300  $\mu\text{s}$ , duty cycle < 3%.
2. Assumes smaller value in numerator.
3. Measured at end points,  $T_A$  and  $T_B$ .
4. These ratings are limiting values above which the serviceability of any semiconductor may be impaired.

©Applied MATERIALS TECHNOLOGY, INC.

# Monolithic Dual N Channel J-FETs

The following summary specifications are Micro Power Systems in-house types  
from which the above industry standard types are derived.

Parameter Units	$ V_{GS1} - V_{GS2} $	$\frac{\Delta V_{GS}}{\Delta T}$	$Y_{fss}$	$\frac{Y_{fs1}}{Y_{fs2}}$	$I_G$	$BV_{GSS}$	$V_p$	$I_{DSS}$	
	mV (Max)	$\mu V/^\circ C$ (Max)	$\mu mho$ (Min)	(Min)	pA (Max)	V (Min)	V (Max)	mA (Min)	mA (Max)
<b>General Purpose</b>									
MP3954A	10	5	1000	0.97	50	60	4.5	0.5	5
MP3954	10	10	1000	0.97	50	60	4.5	0.5	5
MP3955	25	25	1000	0.97	50	60	4.5	0.5	5
MP3956	25	50	1000	0.97	50	60	4.5	0.5	5
MP3958	25	100	1000	0.97	50	60	4.5	0.5	5
<b>Low Leakage</b>									
MP830	25	5	70	0.95	0.1	40	4.5	0.06	1
MP831	25	10	70	0.95	0.1	40	4.5	0.06	1
MP832	25	20	70	0.95	0.1	40	4.5	0.06	1
MP833	25	75	70	0.95	0.5	40	4.5	0.06	1
MP5905	25	75	70	0.95	3	40	4.5	0.06	1
MP5906	25	5	70	0.95	1	40	4.5	0.06	1
MP5907	25	10	70	0.95	1	40	4.5	0.06	1
MP5908	25	20	70	0.95	1	40	4.5	0.06	1
MP5909	25	40	70	0.95	1	40	4.5	0.06	1
<b>Low Noise <math>\leq 10 \text{ nV}/\sqrt{\text{Hz}}</math></b>									
MP840	5	5	1000	0.97	50	60	4.5	0.5	5
MP841	5	10	1000	0.97	50	60	4.5	0.5	5
MP842	25	40	1000	0.97	50	60	4.5	0.5	5
<b>Ultra Low Noise*</b>									
MP843	1	5	1000	0.97	50	60	3.5	0.5	5
MP844	5	10	1000	0.97	50	60	3.5	0.5	5
MP845	15	25	1000	0.97	50	60	3.5	0.5	5
<b>Higher Transconductance</b>									
MP5911	10	20	4000	0.95	50	35	5.0	—	40
MP5912	15	40	4000	0.95	50	35	5.0	—	40
MP5912C	40	40	4000	0.95	50	35	5.0	—	40

# Monolithic Dual PNPs and NPNs

The following summary specifications are Micro Power Systems in-house types  
from which the above industry standard types are derived.

Parameter Condition	$h_{FE1}/h_{FE2}$	$ V_{BE1} - V_{BE2} $	$ \Delta(V_{BE1} - V_{BE2}) $	$ I_{B1} - I_{B2} $	$h_{FE}$	$BV_{CEO}$	$I_{CBO}$	$C_{OB}$	$f_T$
	$I_C = 10 \mu A$ @ 5V (Typ %)	$I_C = 10 \mu A$ (Max)	$T_A = -55^\circ C$ to $+125^\circ C$ (Max)	$I_C = 10 \mu A$ @ 5V (Max)	$I_C = 10 \mu A$ @ 5V (Min/Max)	$I_B = 0$ $I_C = 5 \mu A$ (Min)	$V_{CB} = 80\%$ $BV_{CEO}$ (Max)	$I_E = 0$ $V_{CB} = 5V$ (Max)	$I_C = 1 \text{ mA}$ @ 5V (Min)
<b>NPN, Super Beta</b>									
MP301	5%	1 mV	$5 \mu V/^\circ C$	1.0 nA	2000/—	18V	10 pA	0.8 pF	100 MHz
MP302	5%	1 mV	$5 \mu V/^\circ C$	5.0 nA	1000/—	35V	10 pA	0.8 pF	100 MHz
MP303	5%	1 mV	$5 \mu V/^\circ C$	1.5 nA	2000/—	10V	10 pA	0.8 pF	100 MHz
<b>NPN, General Purpose</b>									
MP310	10%	3 mV	$15 \mu V/^\circ C$	—	150/—	25V	0.2 nA	2 pF	—
MP311	5%	1 mV	$5 \mu V/^\circ C$	10 nA	150/—	45V	0.2 nA	2 pF	200 MHz
MP312	5%	0.5 mV	$2 \mu V/^\circ C$	5 nA	200/—	60V	0.2 nA	2 pF	200 MHz
MP313	5%	1 mV	$5 \mu V/^\circ C$	10 nA	400/1000	45V	0.2 nA	2 pF	200 MHz
<b>NPN, Log Conformance</b>									
$\Delta r_e 1 \text{ ohm typ}$									
MP318	5%	1 mV	$5 \mu V/^\circ C$	10 nA	150/600	25V	0.2 nA	2 pF	200 MHz
<b>PNP, General Purpose</b>									
MP350	10%	5 mV	$20 \mu V/^\circ C$	—	100/—	25V	0.2 nA	2 pF	—
MP351	5%	1 mV	$5 \mu V/^\circ C$	10 nA	150/600	45V	0.2 nA	2 pF	200 MHz
MP352	5%	0.5 mV	$2 \mu V/^\circ C$	5 nA	200/600	60V	0.2 nA	2 pF	200 MHz
<b>PNP, Log Conformance</b>									
$\Delta r_e 1.2 \text{ ohm typ}$									
MP358	5%	1 mV	$5 \mu V/^\circ C$	10 nA	100	20V	0.2 nA	2 pF	200 MHz
<b>PNP, High Speed Linear</b>									
MP360	10%	5 mV	$20 \mu V/^\circ C$	—	100/—	25V	0.2 nA	1 pF	—
MP361	5%	1 mV	$5 \mu V/^\circ C$	10 nA	150/600	45V	0.2 nA	1 pF	200 MHz
MP362	5%	0.5 mV	$2 \mu V/^\circ C$	5 nA	200/600	60V	0.2 nA	1 pF	200 MHz