## MR 910 SERIES

## Designers Data Sheet

## SUBMINIATURE SIZE, AXIAL LEAD MOUNTED FAST RECOVERY POWER RECTIFIERS

. . . designed for special applications such as dc power supplies, inverters, converters, ultrasonic systems, choppers, low RF interference and free wheeling diodes. A complete line of fast recovery rectifiers having typical recovery time of 500 nanoseconds providing high efficiency at frequencies to 100 kHz.

# FAST RECOVERY POWER RECTIFIERS

50-1000 VOLTS 3 AMPERE



#### MAXIMUM RATINGS MR 910 911 914 916 917 918 Symbol 912 Peak Repetitive Reverse 100 200 400 1000 Volts VRRM 600 800 Voltage Working Peak Reverse VRWM Voltage DC Blocking Voltage ٧R Non-Repetitive Peak Reverse VRSM 100 200 1000 1200 Volts 300 525 800 Voltage **Average Rectified Forward Current** ō (Single phase resistive load, $T_A = 90^{\circ}C$ ) Non-Repetitive Peak Surge Current IFSM Amp faurge applied at rated load conditions) Tj,T<sub>stg</sub> -65 to +175 -°C Operating and Storage Junction Temperature Range

#### THERMAL CHARACTERISTICS

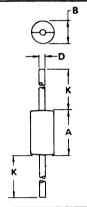
Cheracteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	ReJA	28	oC/M

#### **ELECTRICAL CHARACTERISTICS**

Symbol	Min	Тур	Max	Unit
٧F	-	0.9	1.1	Volts
٧F	-	1.04	1.25	Volts
I <sub>R</sub>	-		10	μΑ
	_	-	300	μΑ
	At.	√F -	VF - 0.9	VF - 0.9 1.1 VF - 1.04 1.25

#### REVERSE RECOVERY CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Mex	Unit
Reverse Recovery Time (I <sub>F</sub> = 1.0 Amp to V <sub>R</sub> = 30 Vdc	ţrı	-		750	ns
Reverse Recovery Current (Ip = 1.0 Amp to Vg = 30 Vdc	IRM(REC)	-	-	3.0	Amp



	MILLI	METERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	9.40	9.65	0.370	0.380	
8	4.83	5.33	0.190	0.210	
0	1.22	1.32	0.048	0.052	
K	26.97	27.23	1.062	1.072	

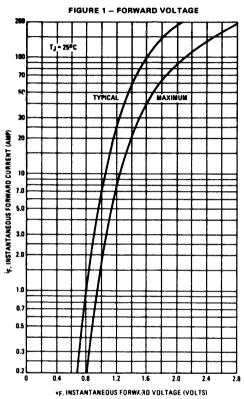
### **MECHANICAL CHARACTERISTICS**

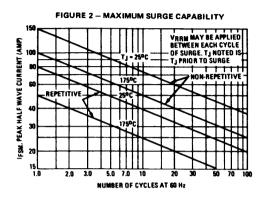
Case: Void Free, Transfer Molded Finish: External Leads are Plated, Leads are readily Solderable Polarity: Indicated by Cathode Band

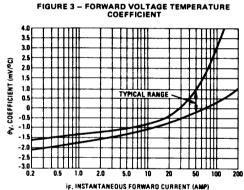
Weight: 1.1 Grams (Approximately)
Maximum Lead Temperature for
Soldering Purposes:

240°C, 1/8" from case for 10 s at 5.0 lb, tension

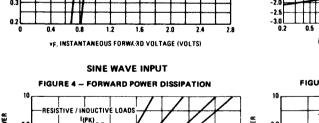
#### STATIC CHARACTERISTICS

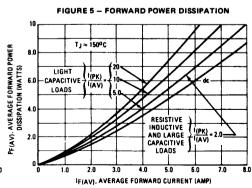






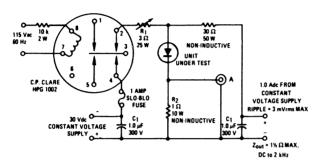
**SQUARE WAVE INPUT** 





#### **DYNAMIC CHARACTERISTICS**

#### FIGURE 6 - REVERSE RECOVERY CIRCUIT

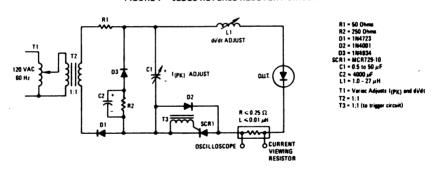


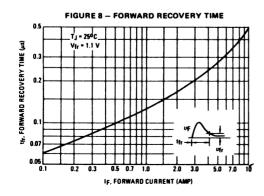
#### MINIMIZE ALL LEAD LENGTHS

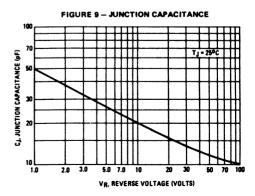
- A TEKTRONIX 545A, K PLUG IN PRE-AMP, P6000 PROBE OR EQUIVALENT
- R1 ADJUSTED FOR 1.4 Ω BETWEEN
  POINT 2 OF RELAY AND RECTIFIER
  INDUCTANCE ~ 38 

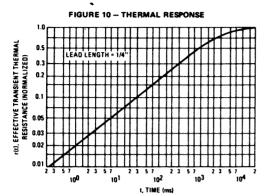
  µH
- R<sub>2</sub> TEN-1 W, 10 Ω, 1% CARBON CORE IN PARALLEL
- TA = 25 +10 oc FOR RECTIFIER

#### FIGURE 7 - JEDEC REVERSE RECOVERY CIRCUIT .









## FIGURE 11 - STEADY STATE THERMAL RESISTANCE . SINGLE LEAD TO HEAT SINK INSIGNIFICANT HEAT FLOW THROUGH OTHER LEAD THERMAL RESISTANCE MAXIMUM - TYPICAL Røjl, THERMAL RESIST JUNCTION-TO-LEAD ! BOTH LEADS TO HEAT SINK, EQUAL LENGTH

#### NOTE 1

To determine maximum junction temperature of the diode in a given situation, the following procedure is recommended:

The temperature of the lead should be measured using a thermocouple placed on the lead as close as possible to the till point. The thermal mass connected to the tip point is normally large amough so that it will not significantly respond to heat surges generated in the dode as a result or placed operation once storage state conditions are achieved. Using the measured value of T<sub>L</sub>, the junction temperature may be determined by:

where  $\Delta T_{JL}$  is the increase in junction temperature above the lead temperature. It may be determined by:

$$\Delta T_{JL} = P_{pk} \cdot R_{\theta JL} \left\{ D + (I - D) \cdot r(t_1 + t_p) + r(t_p) - r(t_1) \right\}$$

L. LEAD LENGTH (INCHES)



#### NOTE 2

Use of the above model permits junction to lead thermal resistance for any mounting configuration to be found. For a given total lead length, lowest values occur when one side of the rectifier is brought as close as possible to the heat sink. Terms in the model signify:

TA = Ambient Temperature T<sub>L</sub> = Lead Temperature

T<sub>C</sub> = Case Temperature

R<sub>BS</sub> = Thermal Resistance, Heat Sink to Ambient R<sub>BL</sub> = Thermal Resistance, Lead to Heat Sink R<sub>BJ</sub> = Thermal Resistance, Juncrigj = Thermal Resistance, Junction to Case
PD = Total Power Dissipation =
PF + PR
PF = Forward Power Dissipation
PR = Reverse Power Dissipation

 $R_{\theta L} = 46^{\circ} \text{C/W/IN}$ . Typically and  $48^{\circ} \text{C/W/IN}$  MaximuR<sub> $\theta J$ </sub> =  $10^{\circ} \text{C/W}$  Typically and  $16^{\circ} \text{C/W}$  Maximum.

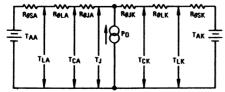
The maximum lead temperature may be found as follows:

TL = TJ(max) - A TJL

 $\Delta T_{JL}$  can be approximated as follows

 $\Delta T_{JL} \approx R_{BJL} \cdot P_D$ ;  $P_D$  is the sum of forward and reverse power dissipation shown in Figures 2 and 4 for sins weve operation and Figures 3 and 5 for square were operation.

#### THERMAL CIRCUIT MODEL (For Heat Conduction Through the Le



#### NOTE 3

Data shown for thermal resistance junction-to-embient  $\{R_{\emptyset,j,k}\}$  for the mountings shown is to be used as typical guideline values for preliminary engineering or in case the tie point temperature

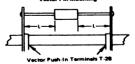
#### TYPICAL VALUES FOR RAIAIN STILL AIR

- 1	MOUNTING	LEAD LENGTH, L (IN)					
1	METHOD	1/8	1/4	1/2	3/4	Peja	
ı	1	50	51	53	55	°C/W	
	2	58	59	61	63	°C/W	
	3		2	18		°C/W	

#### MOUNTING METHOD 1

P.C. Board Where Available Co

### MOUNTING METHOD 2 Vector Pin Mounting



#### MOUNTING METHOD 3

