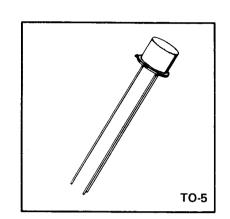
#### HIGH-LEVEL GATE-TURNOFF SCR

## SILICON PLANAR

# HIGH-LEVEL GATE TURNOFF (GTO) TRIODE THYRISTOR

(GATE TURNOFF SCRs)



#### **DESIGN FEATURES**

- Pulse turnoff to 5A
- Typical 5A turnoff gain of 10 15
- Blocking voltages to 400V
- Typical Toff under 5μs
- Operating temperature to +125°C

Transitron's RTGD02 series high-level gate turnoff (GTO) thyristors are unique in combining the high power characteristics of SCRs with the on-off switching flexibility of conventional power transistors without the need for continuous base (gate) current. Unlike conventional SCRs, the GTO is characterized in terms of being triggered off as well as on. The units offer reliable, simplified techniques for bi-stable switching including such uses as an interface element between low level digital logic and high level DC power control. They offer many advantages in applications involving solenoid driving, high voltage and high current pulse switching, and high level logic design.

#### REPETITIVE PEAK OFF-STATE VOLTAGE (VDRM) AND REPETITIVE PEAK REVERSE VOLTAGE (VRRM)

Symbol	RTGD 0206	RTDG 0210	RTDG 0220	RTDG 0230	RTDG 0240	Test Conditions
V <sub>DRM</sub> - VOLTS	60	100	200	300	400	T <sub>C</sub> = 125 <sup>o</sup> C &
V <sub>RRM</sub> – VOLTS	60	100	200	300	400	RGK = 1.0 kilohms

#### ABSOLUTE MAXIMUM RATINGS @ TC = 80°C

Definitions	Symbol	Limits
Average On-State Current RMS On-State Current Peak One-Cycle Surge Current (60 Hz) Peak Reverse Gate Voltage Peak Gate Power Average Gate Power Operating Temperature Range Storage Temperature Range	IT(AV) IT(RMS) ITSM VGRM PGM PG(AV) Top Tstg	1.0A 1.6A 10A 5 500 mW 100 mW -65 to +125°C -65 to +150°C

#### HIGH-LEVEL GATE-TURNOFF SCR

#### **ELECTRICAL CHARACTERISTICS**

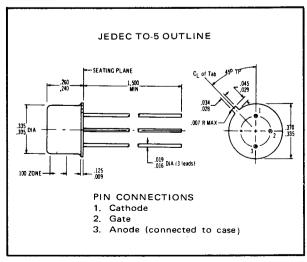
PARAMETER		LIMITS		TEST CONDITIONS				
Symbol	Units	Definitions	Min.	Max.	T °C	R <sub>GK</sub>	V <sub>A</sub> A volts	Other Conditions
V <sub>TM</sub>	Volts	Max. On-State Voltage	_	3	25	_	_	I <sub>TM</sub> = 5 Amps peak
	B	_	10	25	1K	VDRM		
DRM	IDRM μA	Rep. Peak Off-State Current	_	100	125	1K	VDRM	
	IRRM μA	Rep. Peak Reverse Current	_	10	25	1K	VRRM	-
'RRM			_	100	125	1K	VRRM	
<sup>I</sup> GT	mA	Gate Trigger Current		15	25	8	6	
VGT	Volts	Gate Trigger Voltage		1.5	25	∞ ″	6	
IGQM	mA	Gate Turn-off Current	_	500	25	∞	6	I <sub>TM</sub> = 5A (pulse)
V <sub>GQM</sub>	Volts	Gate Turn-off Voltage		5	25	∞	6	I <sub>TM</sub> = 5A (pulse)
Ιн	mA	Holding Current	-	50	25	1K	6	
IGR	μΑ	Gate Reverse Current	_	10	25	8	OPEN	V <sub>GC</sub> = -5 Volts
dv/dt	V/μs	Rate of rise of VDRM	100*	_	25	1K	VDRM	

\*Typical

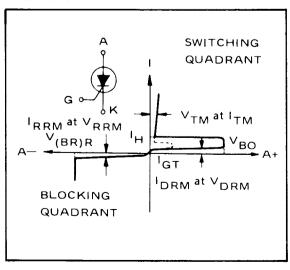
NOTE

FOR CHARACTERISTIC CURVES FOR THIS FAMILY REFER
TO THE END OF THIS GROUP OF SPECIFICATIONS.

#### **PACKAGING DATA**



#### V-I CHARACTERISTICS



### SILICON PLANAR

## LIGHT SENSITIVE REVERSE BLOCKING TRIODE THYRISTORS

TO-18

(LIGHT SENSITIVE SCRs)

#### **DESIGN FEATURES**

- Light sensitivity typically 100 foot-candles
- Blocking voltage to 200V
- Current range to 400 mA RMS
- Peak pulse current to 40 A
- Operating temperature to +125°C

Transitron's new RTPC 01 Series light activated SCR's are designed specifically for those industrial and consumer applications where excellent light sensitivity, superior electrical performance, high reliability, and low cost are companion requirements. These new photo SCR's combine both the light sensing and power amplification function on a single silicon planar chip to eliminate complicated circuitry associated with other light sensitive components. The result is a photo device which provides a whole new set of simplified solutions to those difficult opto-electronic problems. These new photo-SCR's are ideal for use in punched card and tape readers, optical tachometers and shaft encoders, flame detectors, intrusion alarms, and a variety of other light control applications.

### REPETITIVE PEAK OFF-STATE VOLTAGE (VDRM) AND REPETITIVE PEAK REVERSE VOLTAGE (VRRM)

Symbol	RTPC 0101	RTPC 0103	RTPC 0106	RTPC 0110	RTPC 0115	RTPC 0120	Test Conditions
V <sub>DRM</sub> – VOLTS	15	30	60	100	150	200	T <sub>A</sub> = 100 <sup>o</sup> C &
V <sub>RRM</sub> - VOLTS	15	30	60	100	150	200	R <sub>GK</sub> = 27 kilohms

#### MAXIMUM RATINGS @ $T_{\Delta} = 75^{\circ}C$

Definitions	Symbol	Limits		
Average On-State Current	I <sub>T(AV)</sub>	250mA		
RMS On-State Current	IT(RMS)	400mA		
Peak One-Cycle Surge Current (60 Hz)	I <sub>TSM</sub>	5.0A		
Peak Reverse Gate Voltage	V <sub>GRM</sub>	5.0V		
Peak Gate Power	PGM	200mW		
Average Gate Power	P <sub>G(AV)</sub>	20mW		
Operating Temperature Range	Top	-65 to +125°C		
Storage Temperature Range	T <sub>stg</sub>	-65 to +150°C		

#### **ELECTRICAL CHARACTERISTICS**

	PARAMETERS		LIMITS		TEST CONDITIONS			
Symbol	Units	Definitions	Min.	Max.	°C	R <sub>GK</sub> ohms	V <sub>AA</sub> volts	Other Conditions
V <sub>TM</sub>	Volts	Max, On-State Voltage	-	1.5	25	_	_	I <sub>TM</sub> = 250 mA peak
l= =	I <sub>DRM</sub> μΑ	Rep. Peak Off-State Current	_	10	25	27K	V <sub>DRM</sub>	
'DRM			_	100	100	27K	V <sub>DRM</sub>	
	I <sub>RRM</sub> μΑ	Rep. Peak Reverse Current	-	10	25	27K	V <sub>DRM</sub>	
'RRM				100	100	27K	V <sub>DRM</sub>	
¹ <sub>G</sub> ⊤	μΑ	Gate Trigger Current		100	25	∞	6.0	
V <sub>GT</sub>	Volts	Gate Trigger Voltage	_	0.8	25	∞ .	6.0	
ŀН	mA	Holding Current	_	2.0	25	27K	6.0	
I <sub>GR</sub>	μΑ	Gate Reverse Current	_	5.0	25	∞	OPEN	V <sub>GC</sub> = -5 Volts
dv/dt	V/μs	Rate of rise of V <sub>DRM</sub>	25*	-	25	27K	V <sub>DRM</sub>	
L <sub>GT</sub>	Ft-C	Light Sensitivity	_	150	25	27K	6	

<sup>\*</sup>TYPICAL

#### **PACKAGING DATA**

